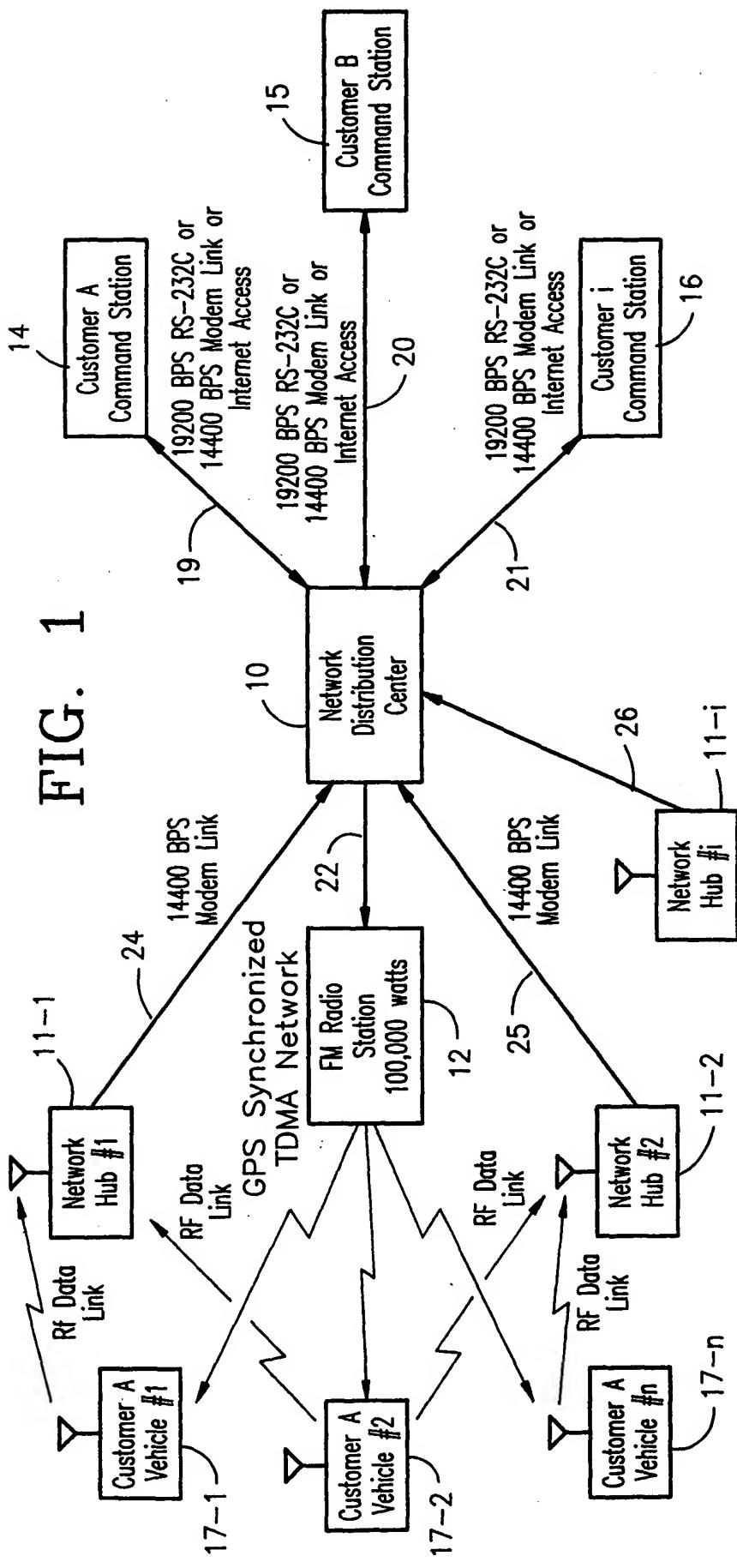
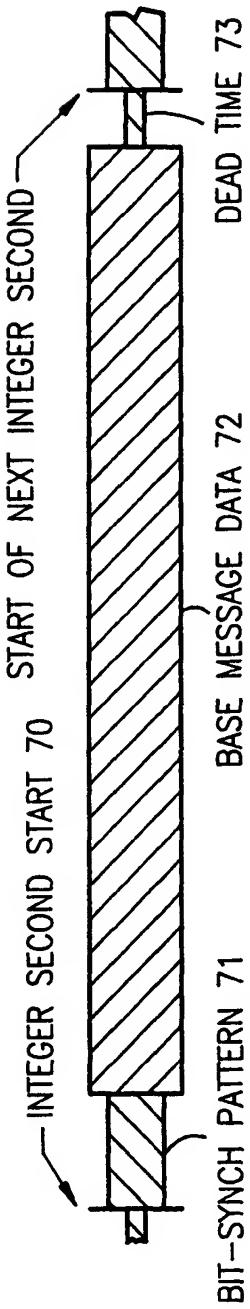


**FIG. 1**



**FIG. 7**



BIT-SYNCH PATTERN 71

BASE MESSAGE DATA 72

DEAD TIME 73

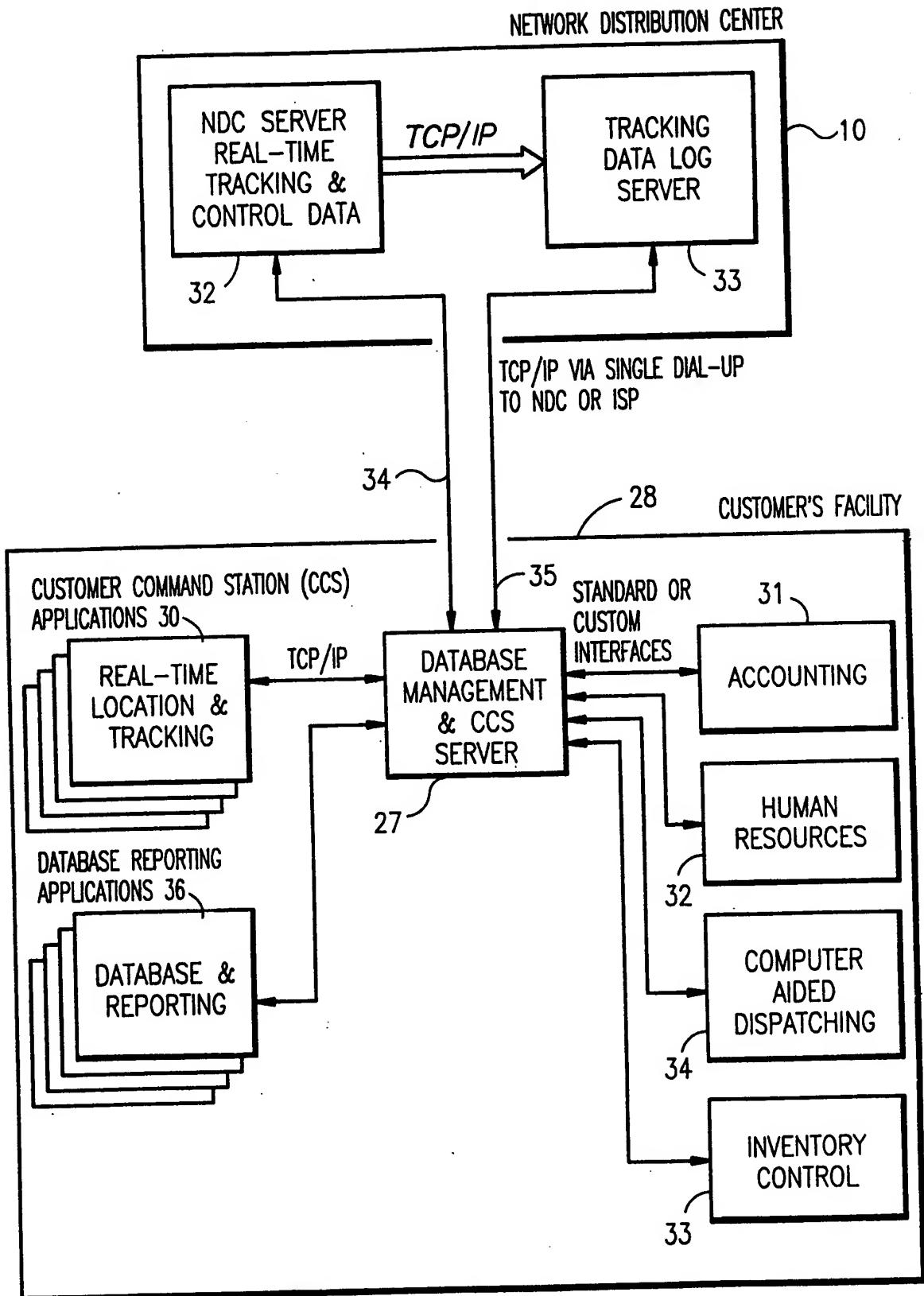


FIG. 2

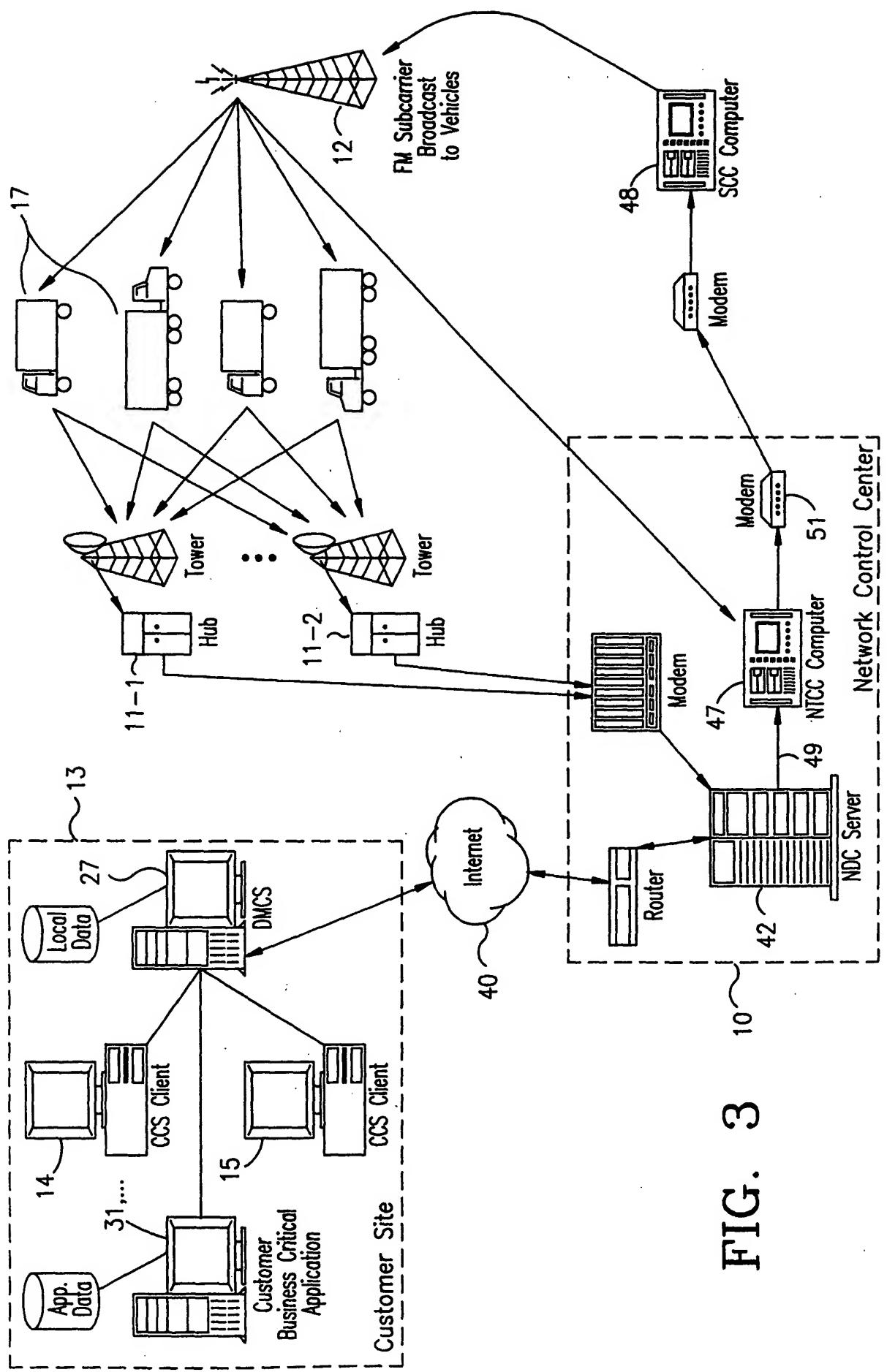


FIG. 3

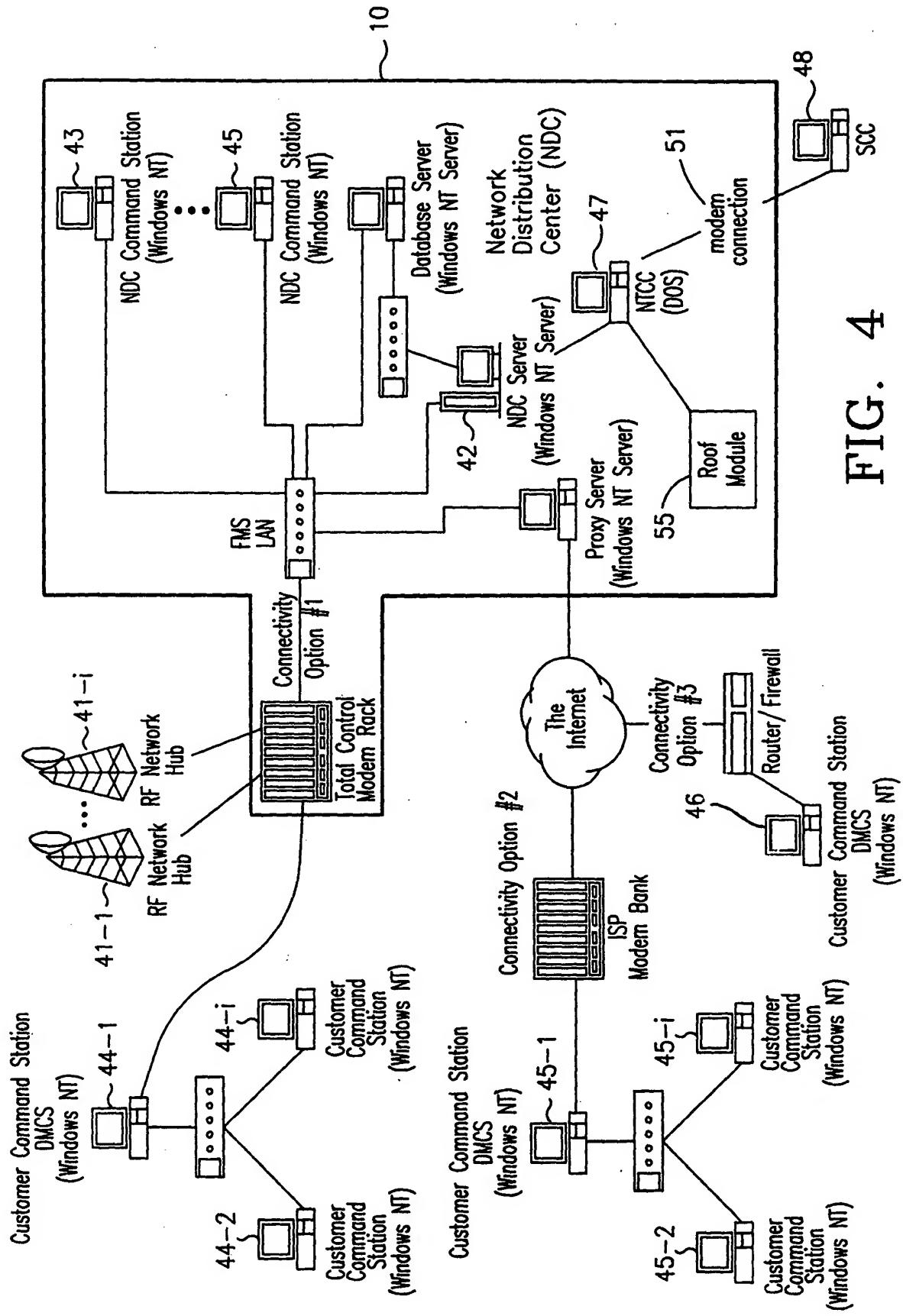


FIG. 4

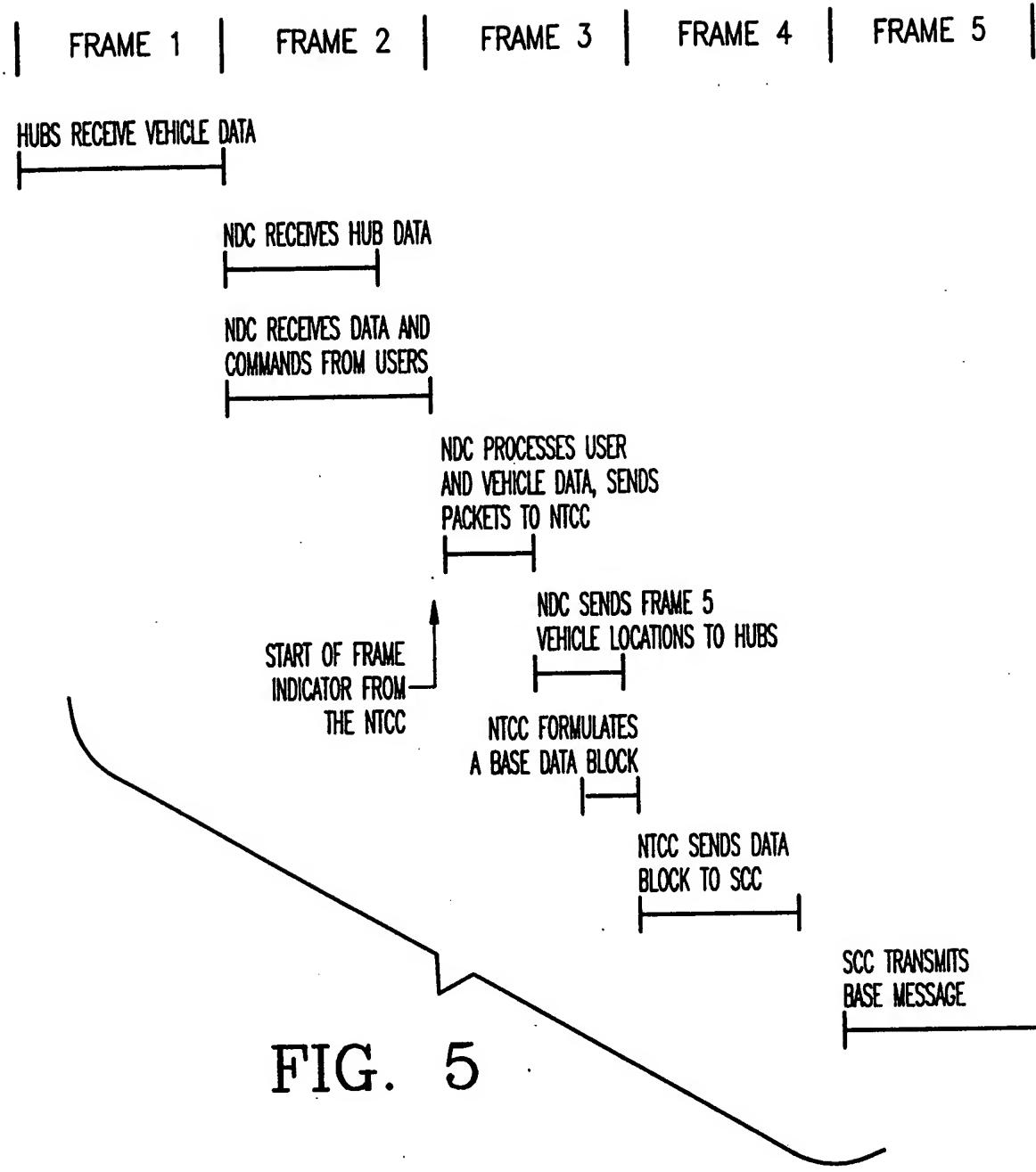


FIG. 8

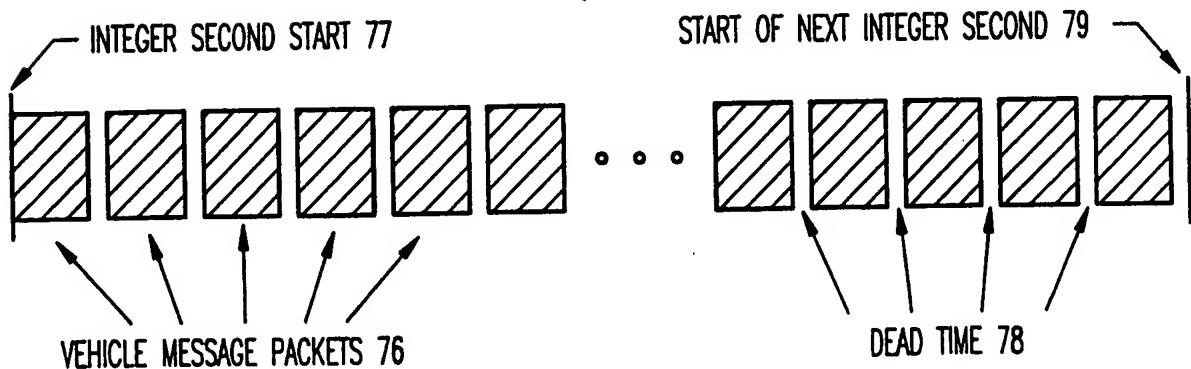


FIG. 6

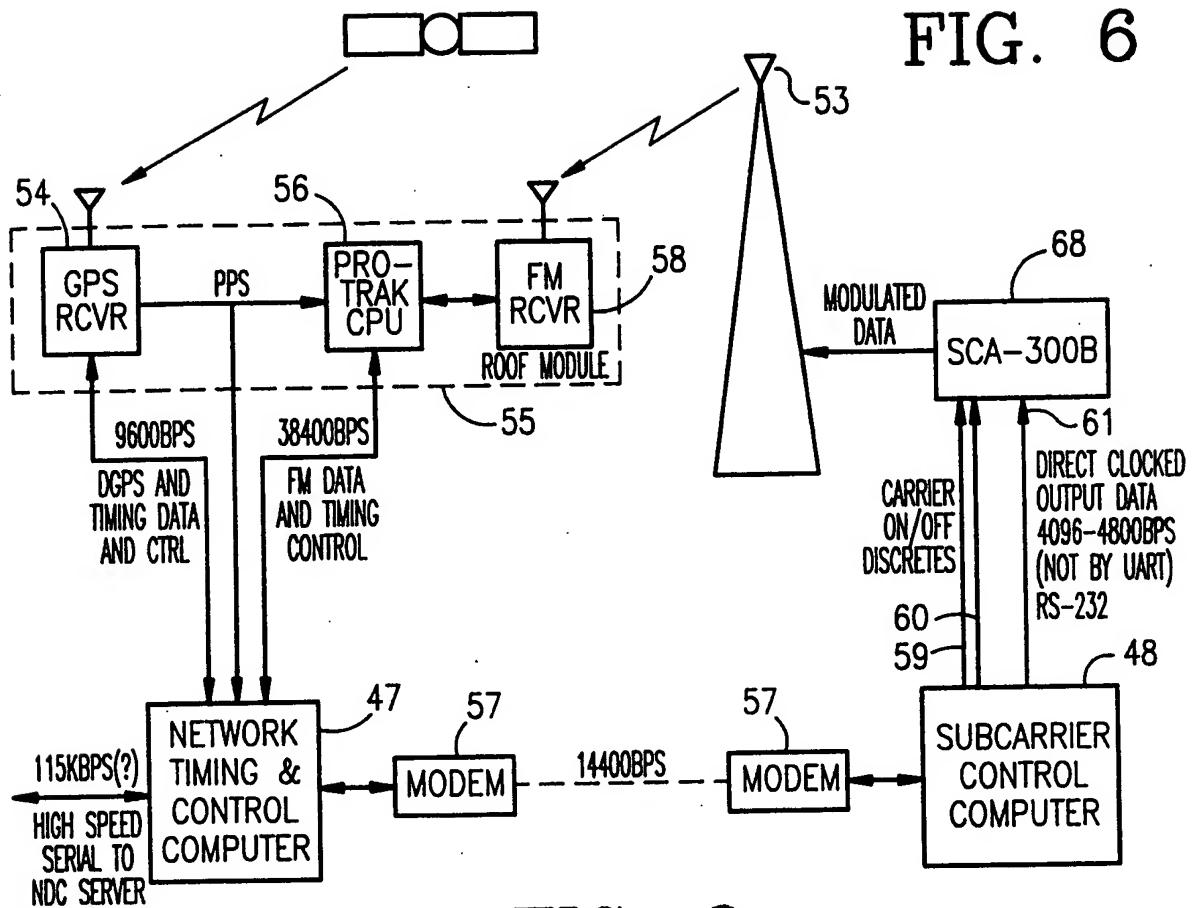
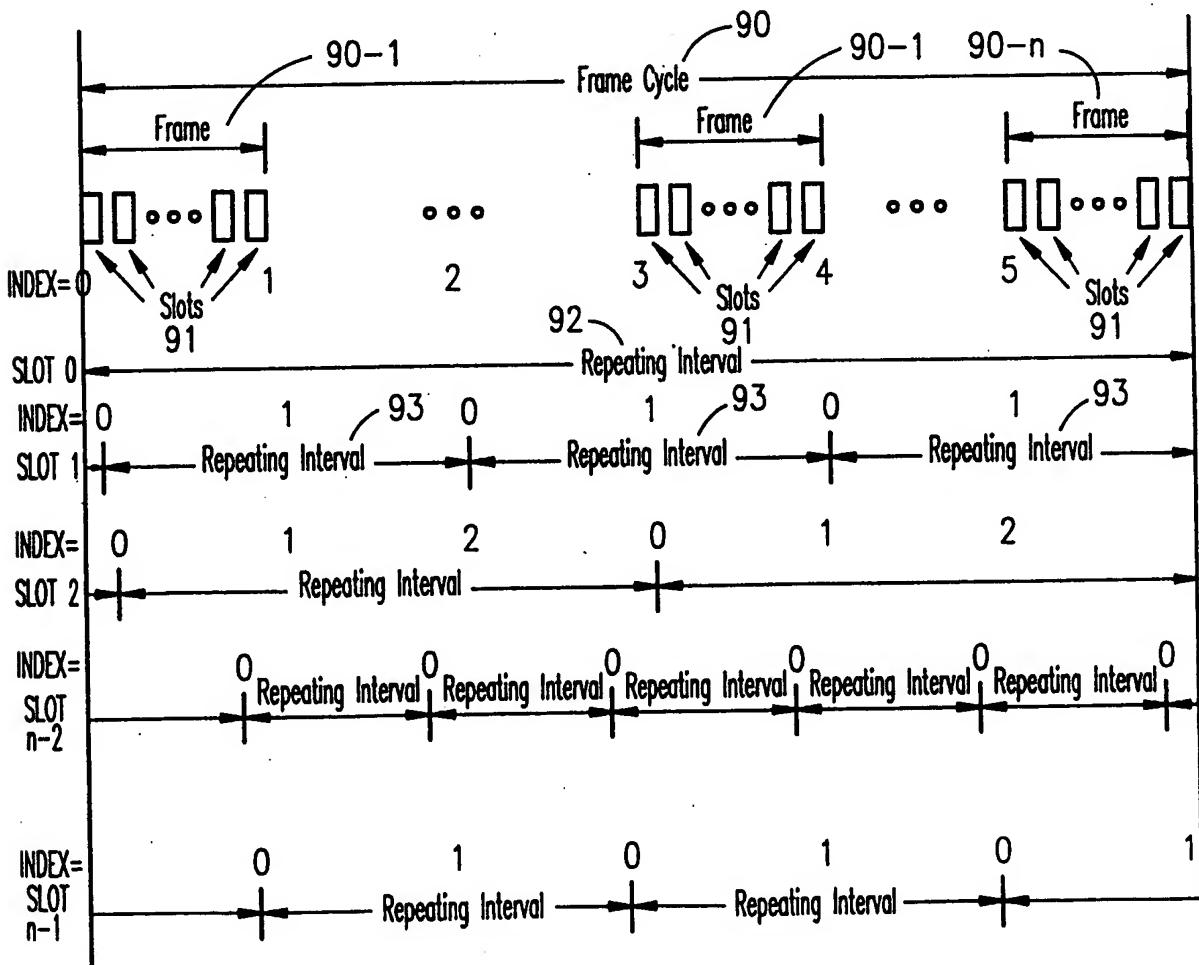


FIG. 9



**FIG. 10**

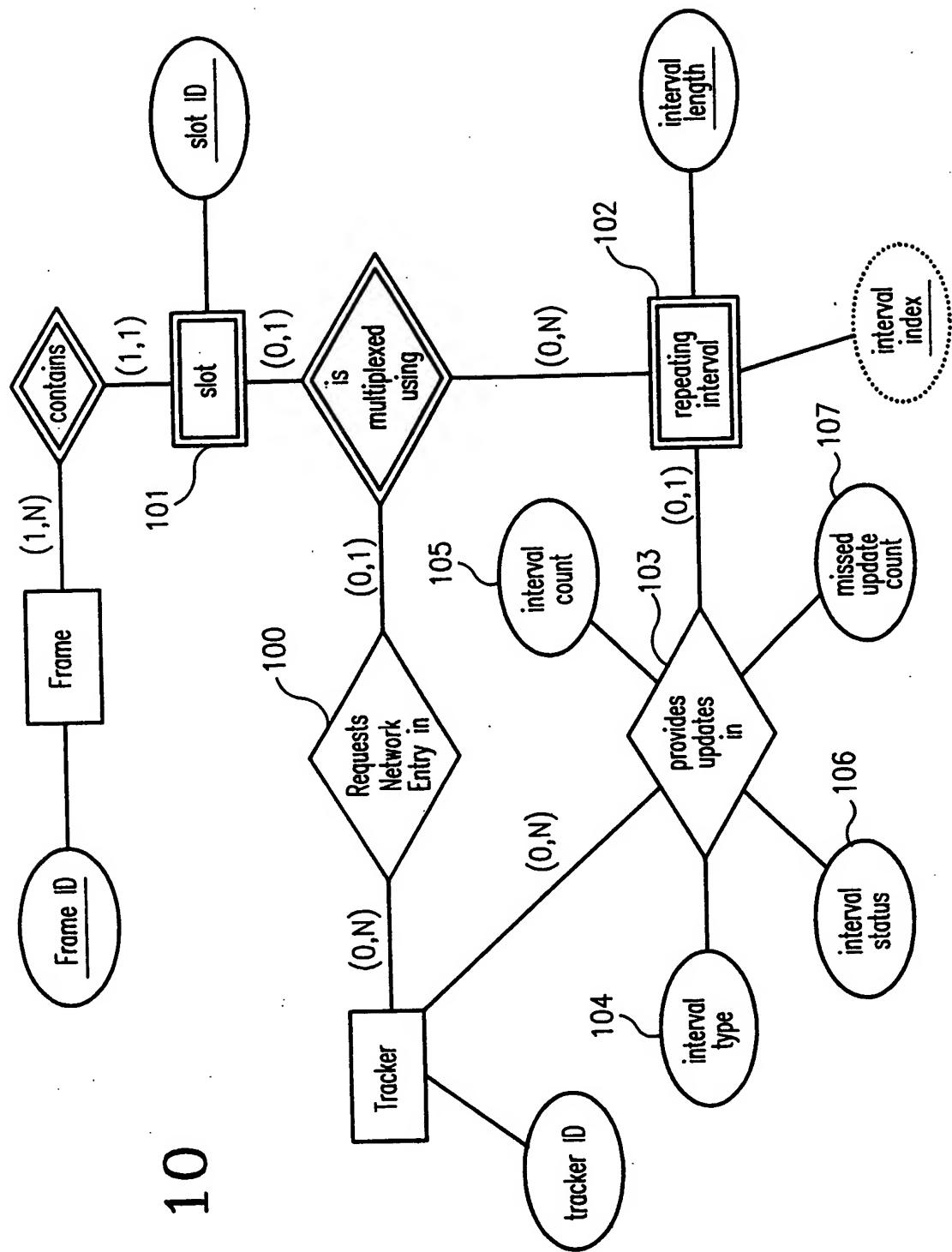


FIG. 11

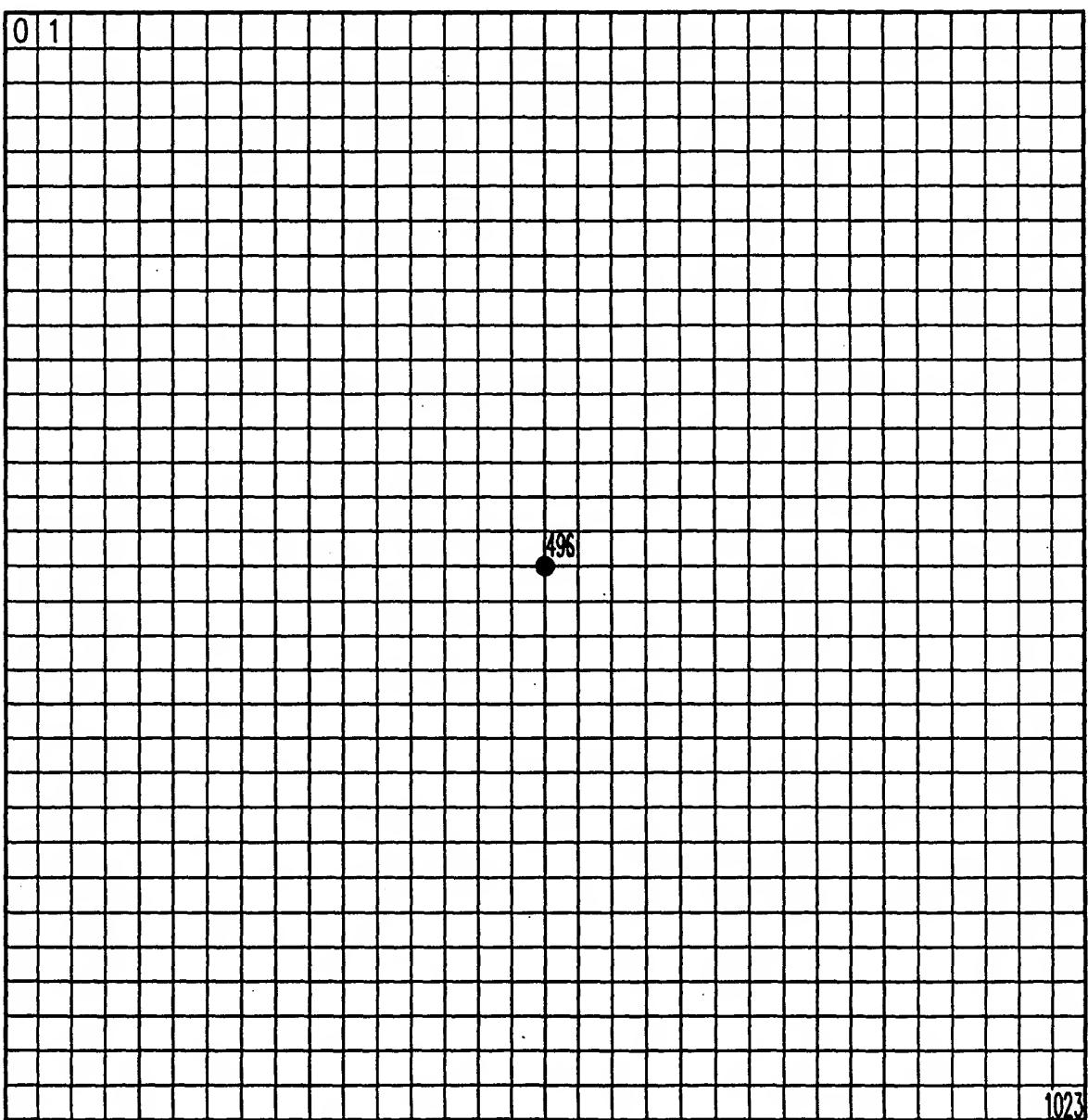


FIG. 12

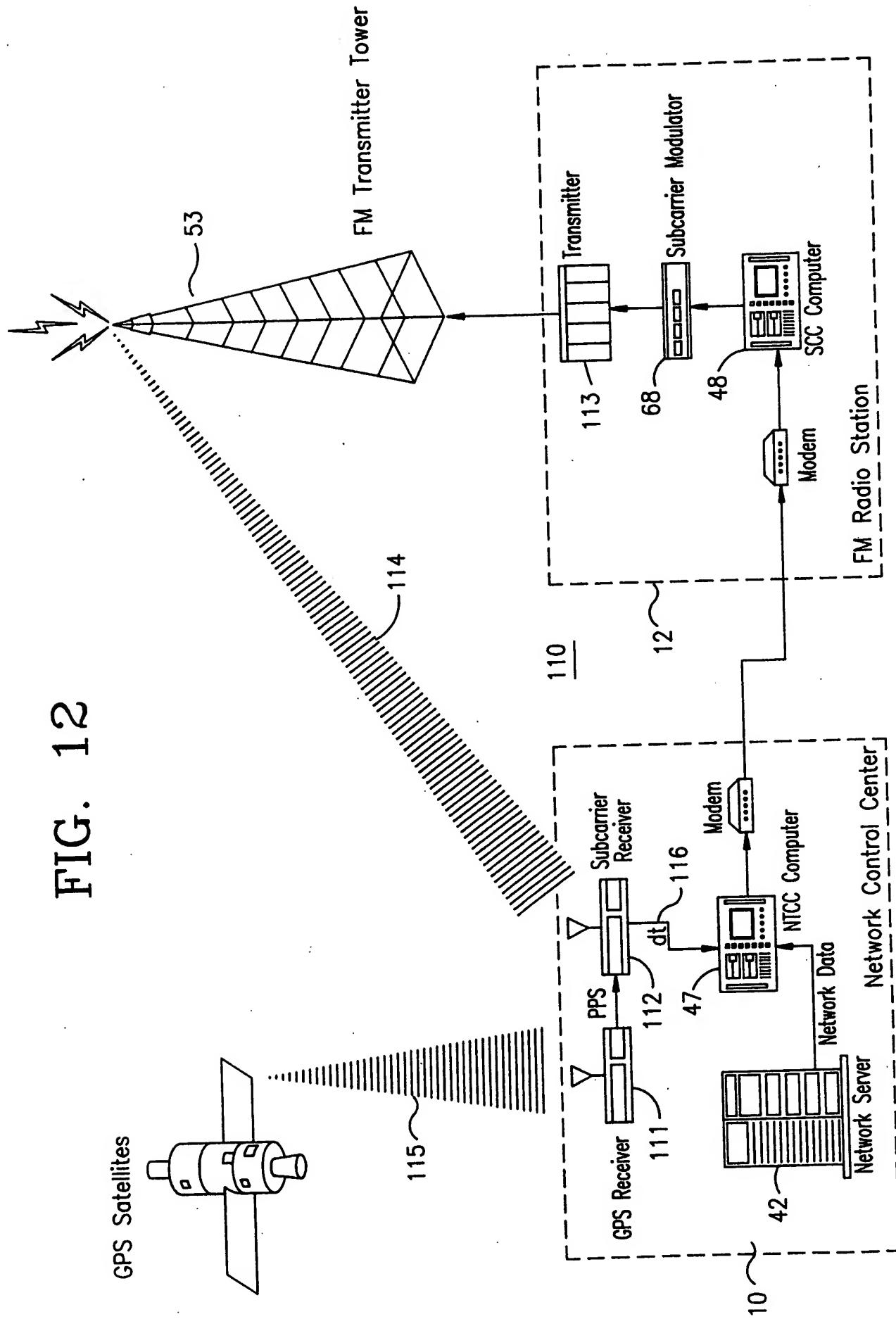


FIG. 13

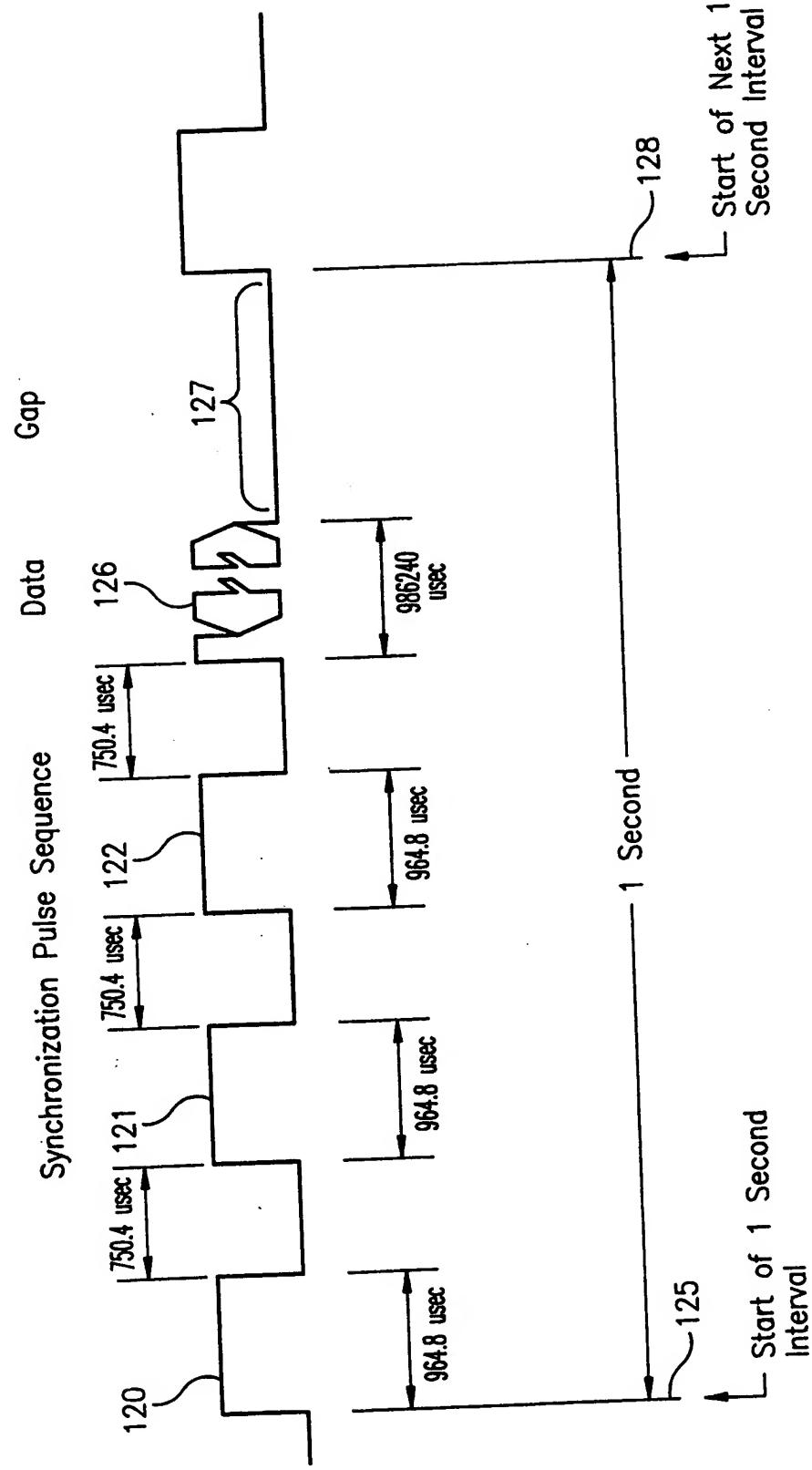


FIG. 14A  
Initialization Mode

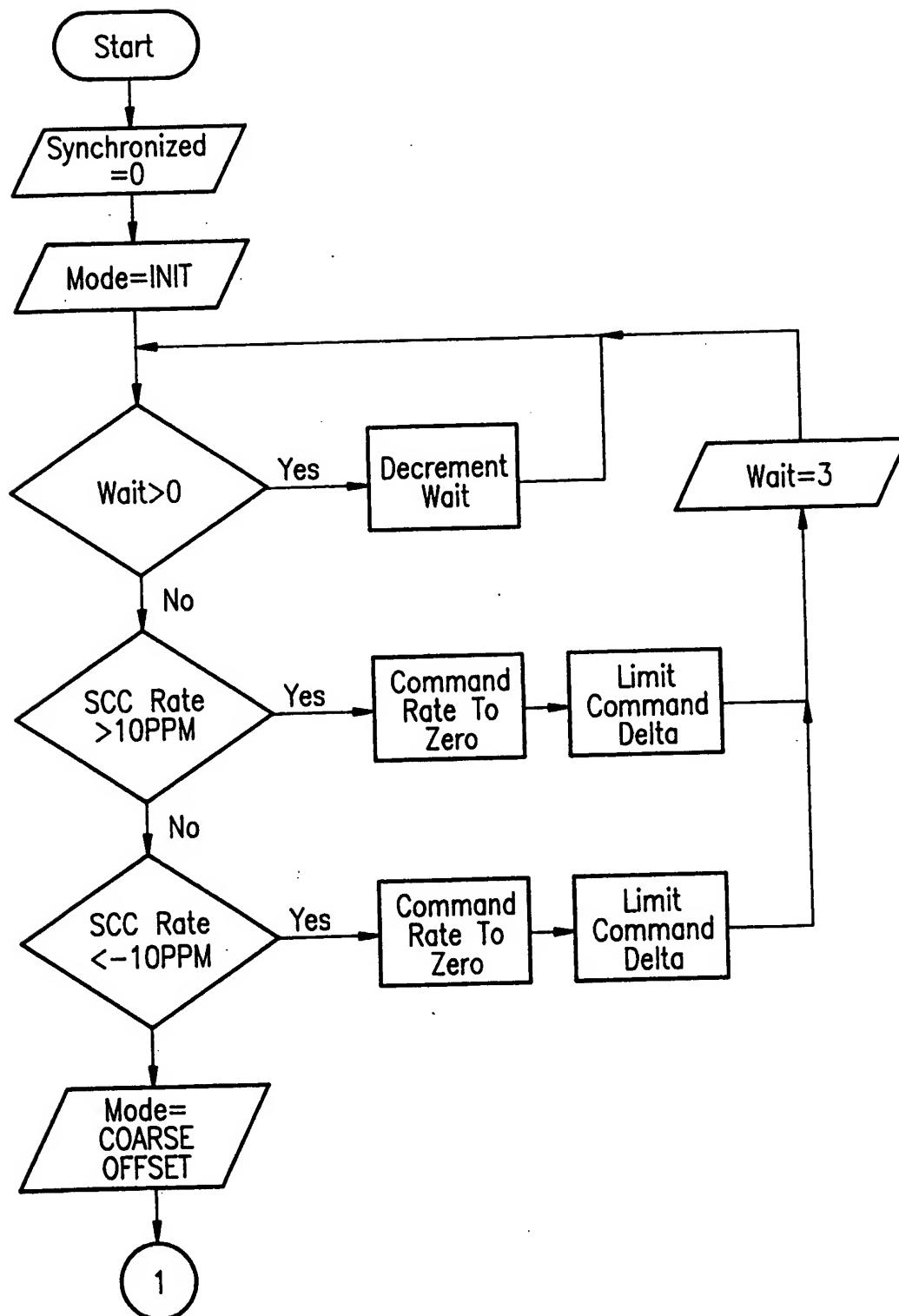
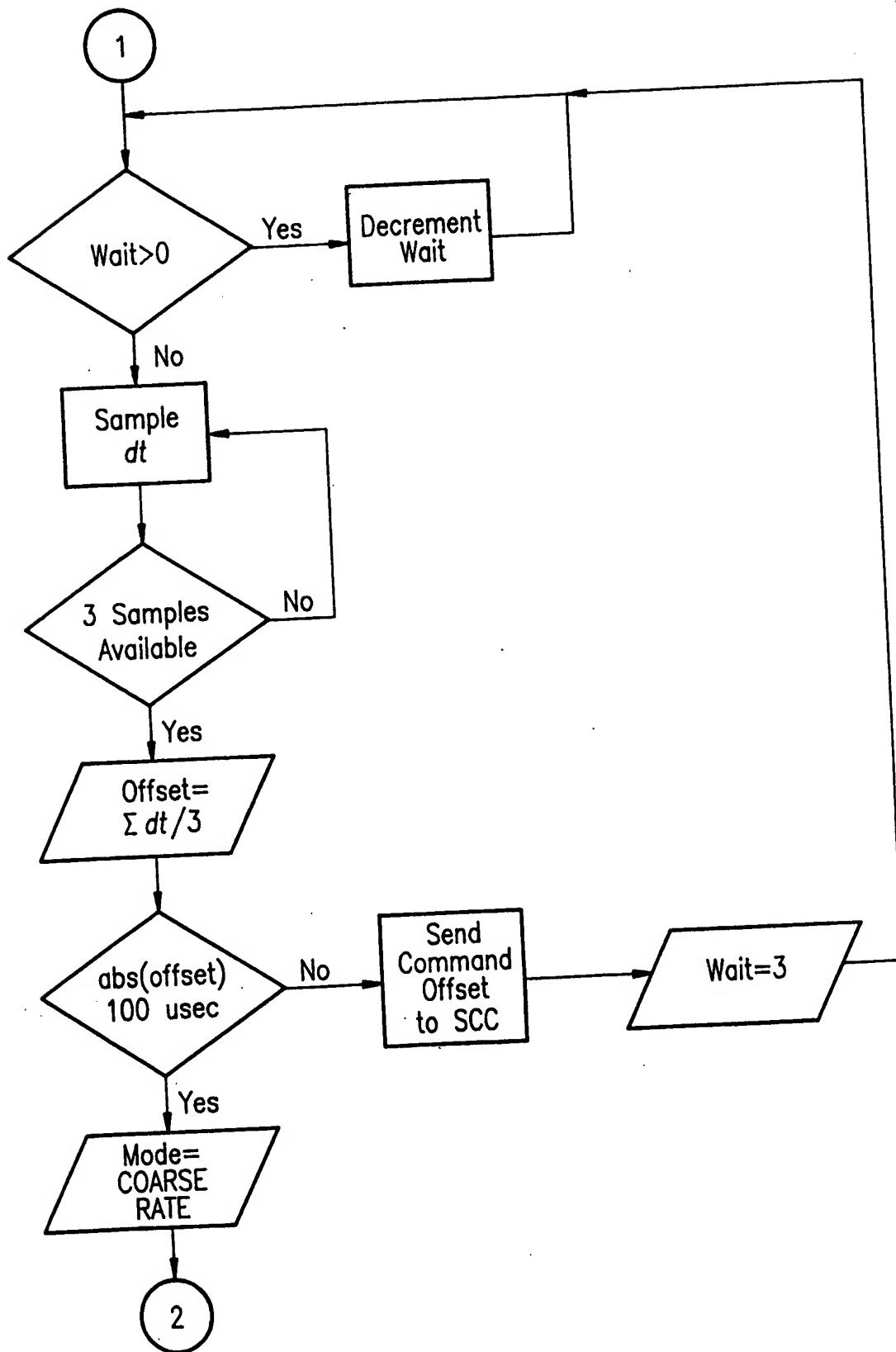


FIG. 14B  
Coarse Offset Mode



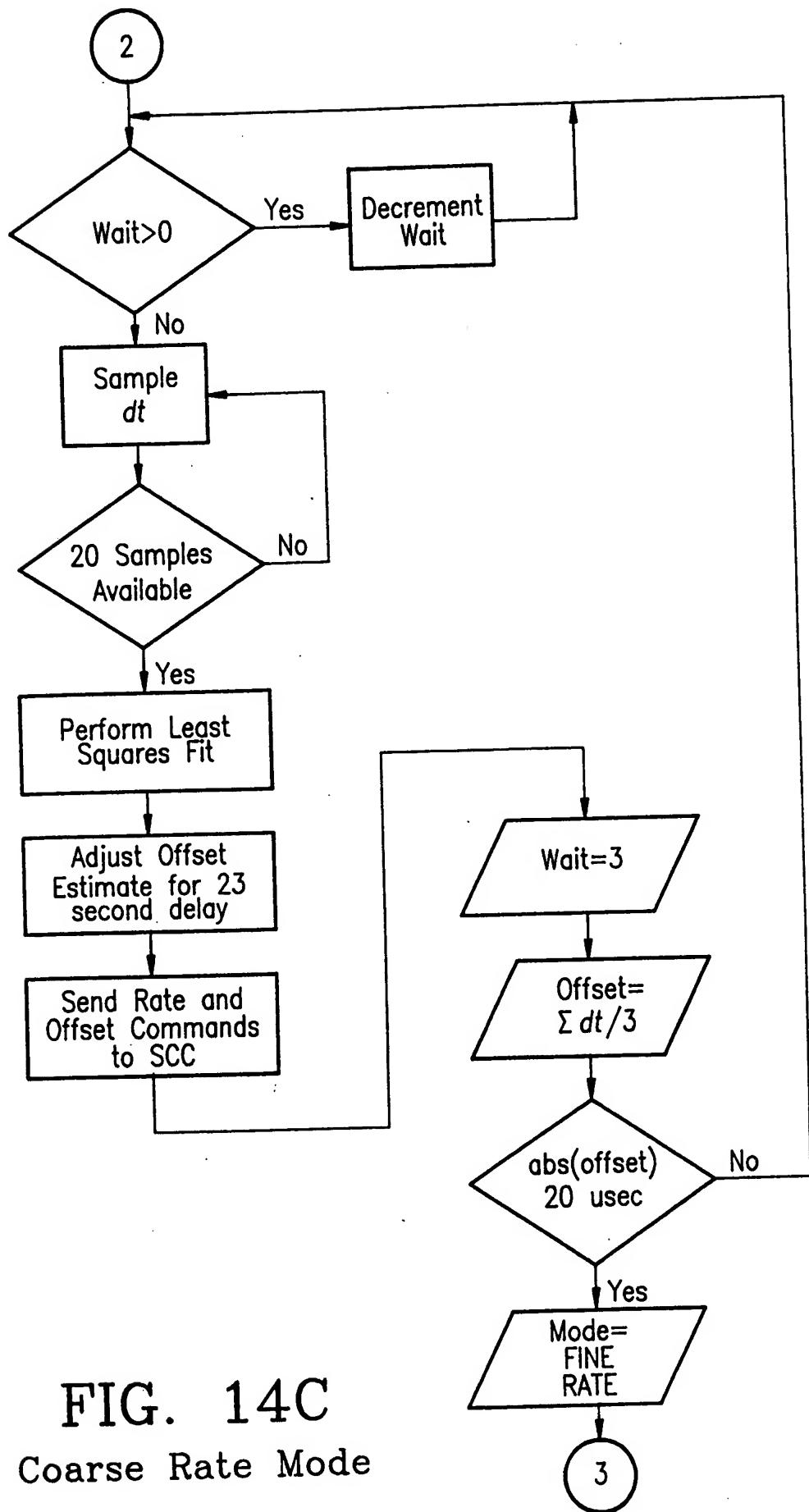


FIG. 14C  
Coarse Rate Mode

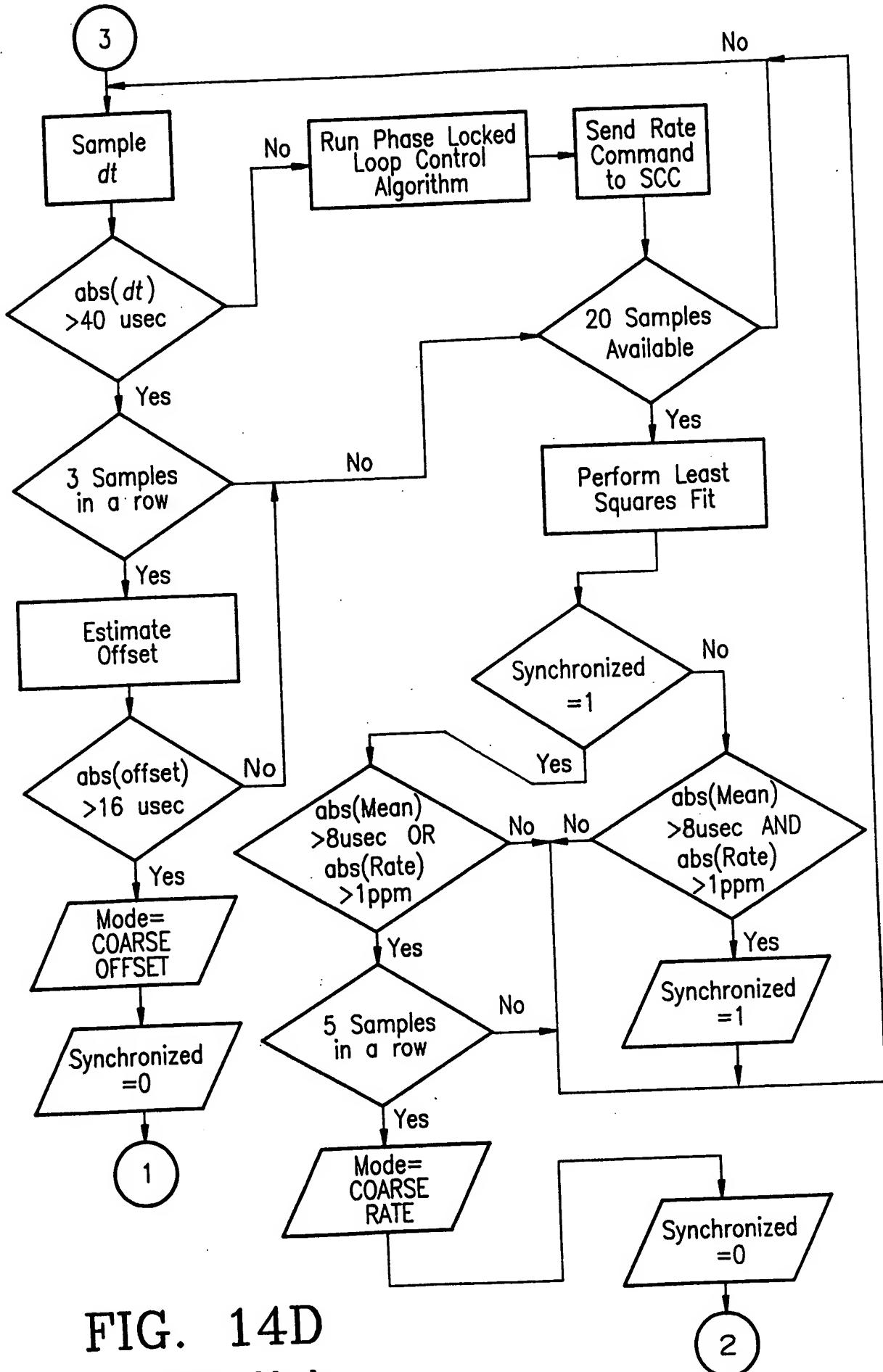


FIG. 14D  
Fine Rate Mode

FIG. 23

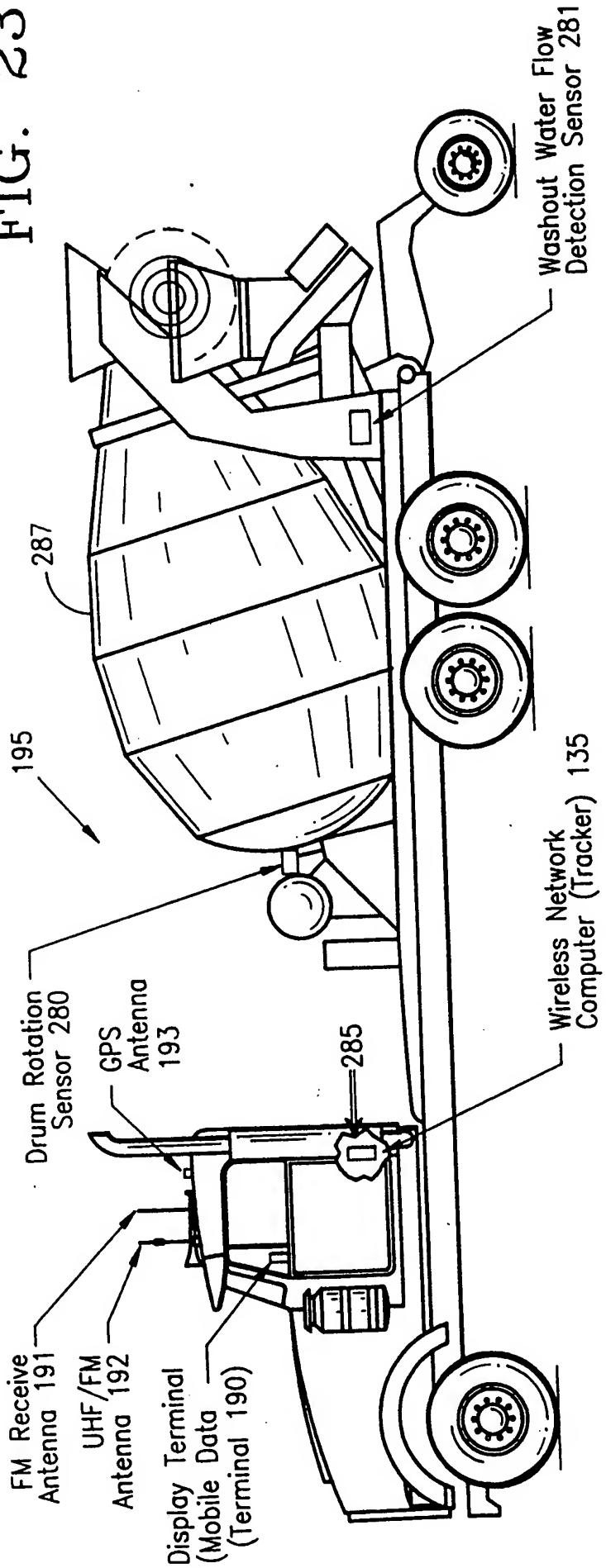
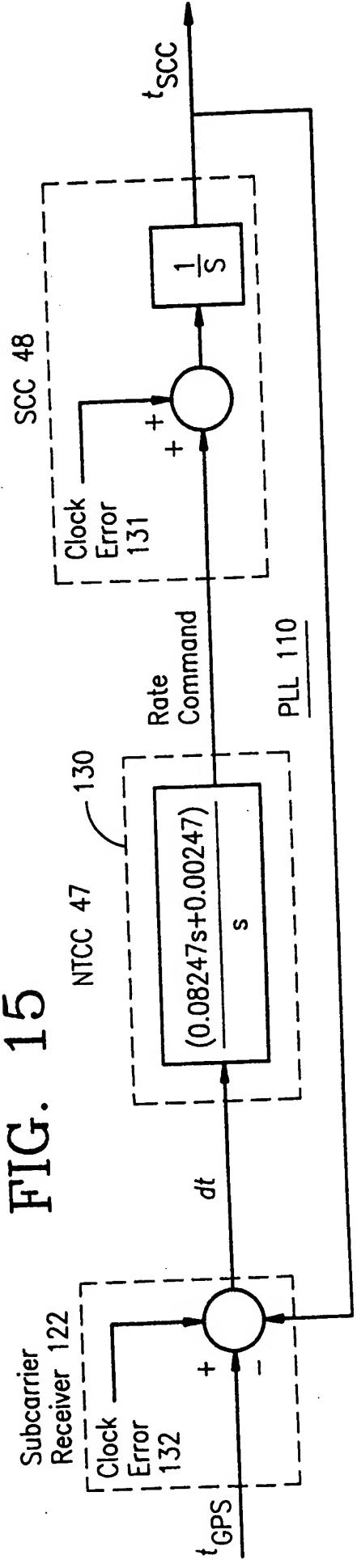
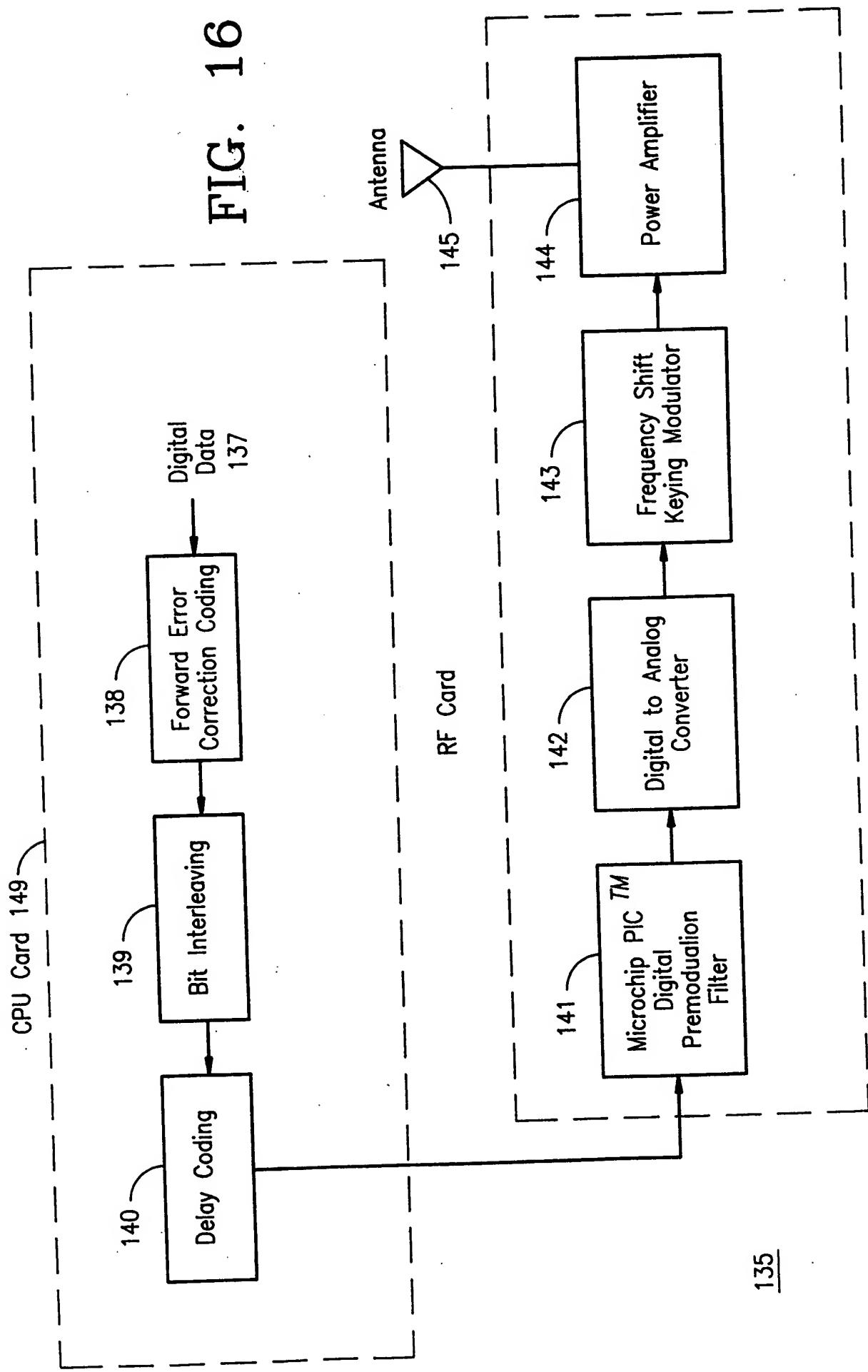


FIG. 15



**FIG. 16**

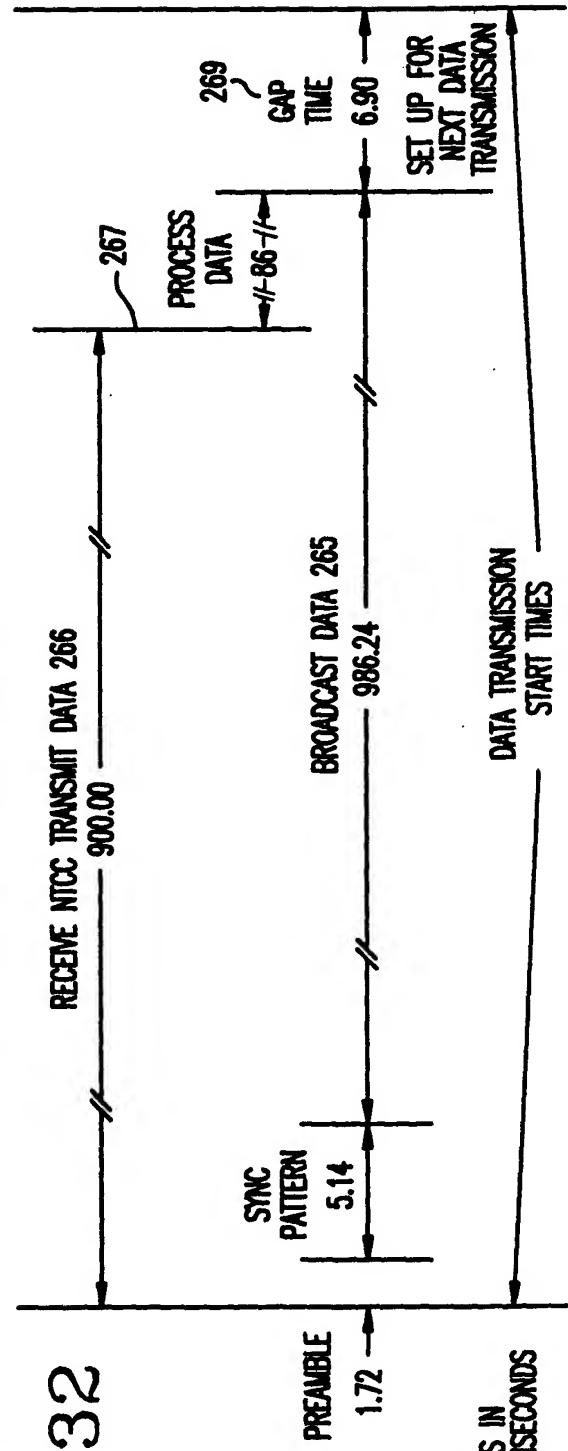


	Words	11	10	9	8	7	6	5	4	3	2	1	0	
		TDMA Transmit Bit Interleaving												
		11	10	9	8	7	6	5	4	3	2	1	0	
0	1/0 000	2/0 001	3/0 002	4/0 003	5/0 004	6/0 005	7/0 006	8/0 007	9/0 008	10/0 009	11/0 010	0/1 011		
1	2/1 012	3/1 013	4/1 014	5/1 015	6/1 016	7/1 017	8/1 018	9/1 019	10/1 020	11/1 021	0/2 022	1/2 023		
2	3/2 024	4/2 025	5/2 026	6/2 027	7/2 028	8/2 029	9/2 030	10/2 031	11/2 032	0/3 033	1/3 034	2/3 035		
3	4/3 036	5/3 037	6/3 038	7/3 039	8/3 040	9/3 041	10/3 042	11/3 043	0/4 044	1/4 045	2/4 046	3/4 047		
4	5/4 048	6/4 049	7/4 050	8/4 051	9/4 052	10/4 053	11/4 054	0/5 055	1/5 056	2/5 057	3/5 058	4/5 059		
5	6/5 060	7/5 061	8/5 062	9/5 063	10/5 064	11/5 065	0/6 066	1/6 067	2/6 068	3/6 069	4/6 070	5/6 071		
6	7/6 072	8/6 073	9/6 074	10/6 075	11/6 076	0/7 077	1/7 078	2/7 079	3/7 080	4/7 081	5/7 082	6/7 083		
7	8/7 084	9/7 085	10/7 086	11/7 087	0/8 088	1/8 089	2/8 090	3/8 091	4/8 092	5/8 093	6/8 094	7/8 095		
8	9/8 096	10/8 097	11/8 098	0/9 099	1/9 100	2/9 101	3/9 102	4/9 103	5/9 104	6/9 105	7/9 106	8/9 107		
9	10/9 108	11/9 109	0/10 110	1/10 111	2/10 112	3/10 113	4/10 114	5/10 115	6/10 116	7/10 117	8/10 118	9/10 119		
10	11/10 120	0/11 121	1/11 122	2/11 123	3/11 124	4/11 125	5/11 126	6/11 127	7/11 128	8/11 129	9/11 130	10/11 131		
11	0/0 132	1/1 133	2/2 134	3/3 135	4/4 136	5/5 137	6/6 138	7/7 139	8/8 140	9/9 141	10/10 142	11/11 143		

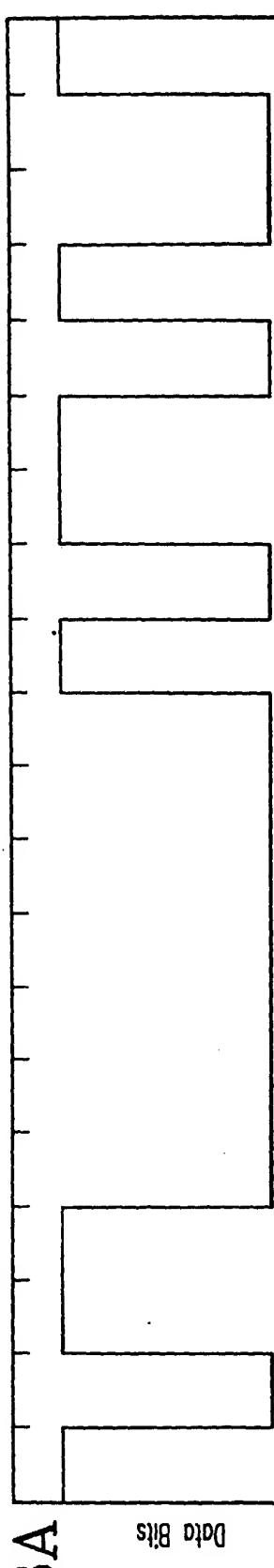
WB Indicates bit,  $B_0$ , of the original code word,  $W$ .  
Words are transmitted MSB first; the small number indicates transmit bit order.

FIG. 32

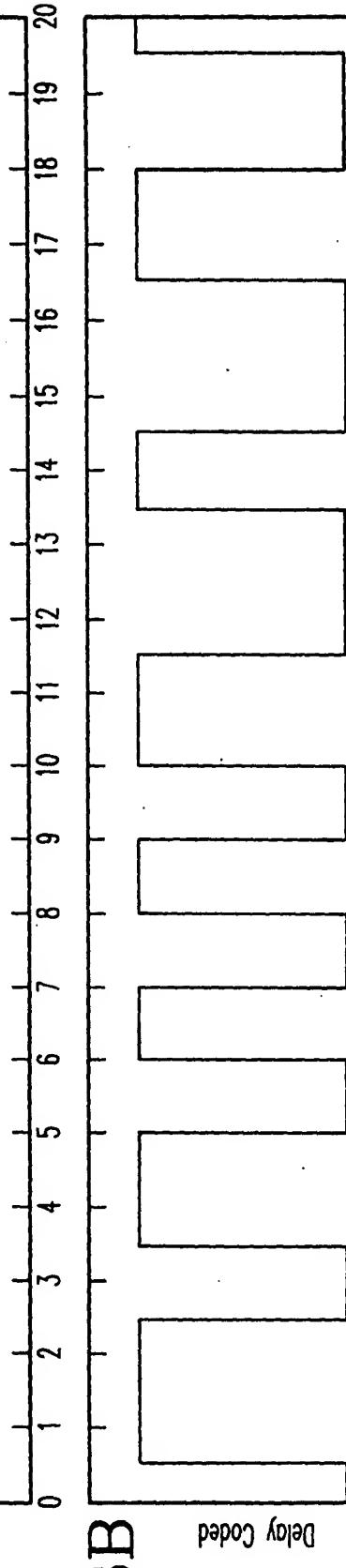
FIG. 17



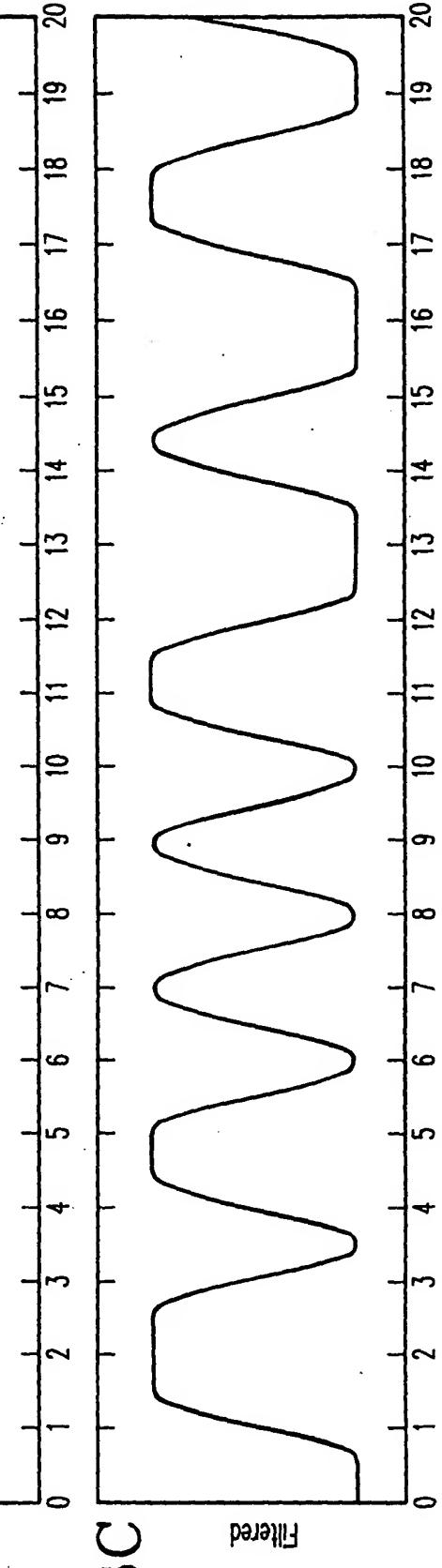
**FIG. 18A**



**FIG. 18B**



**FIG. 18C**



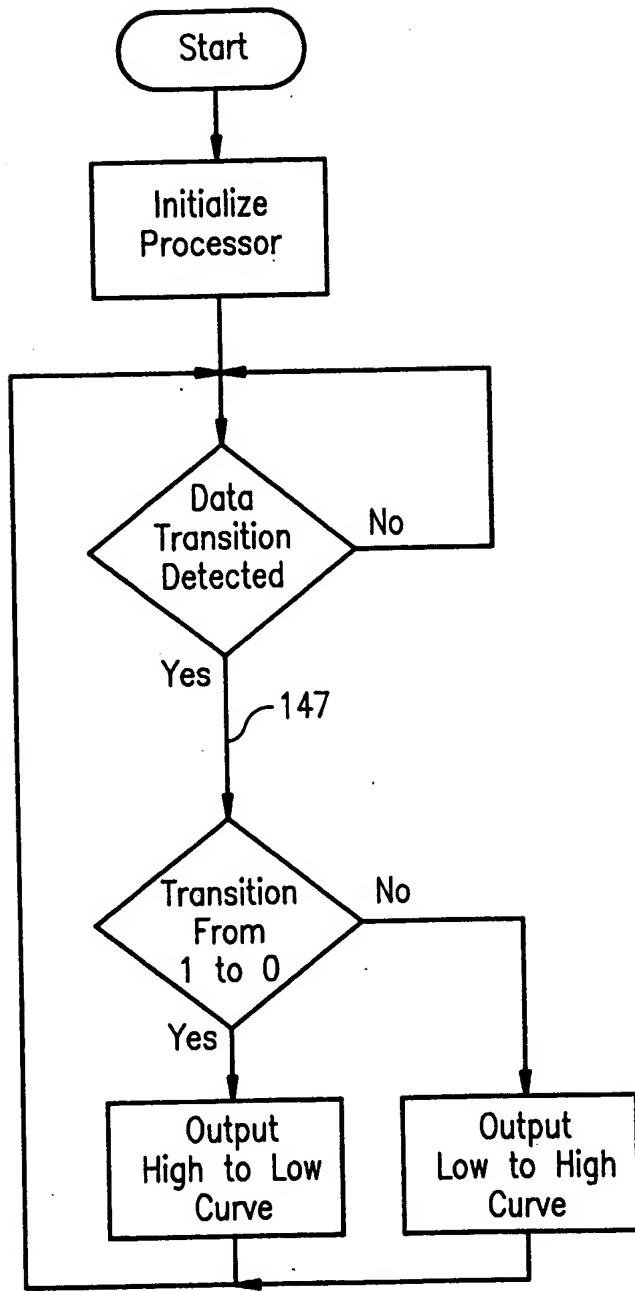
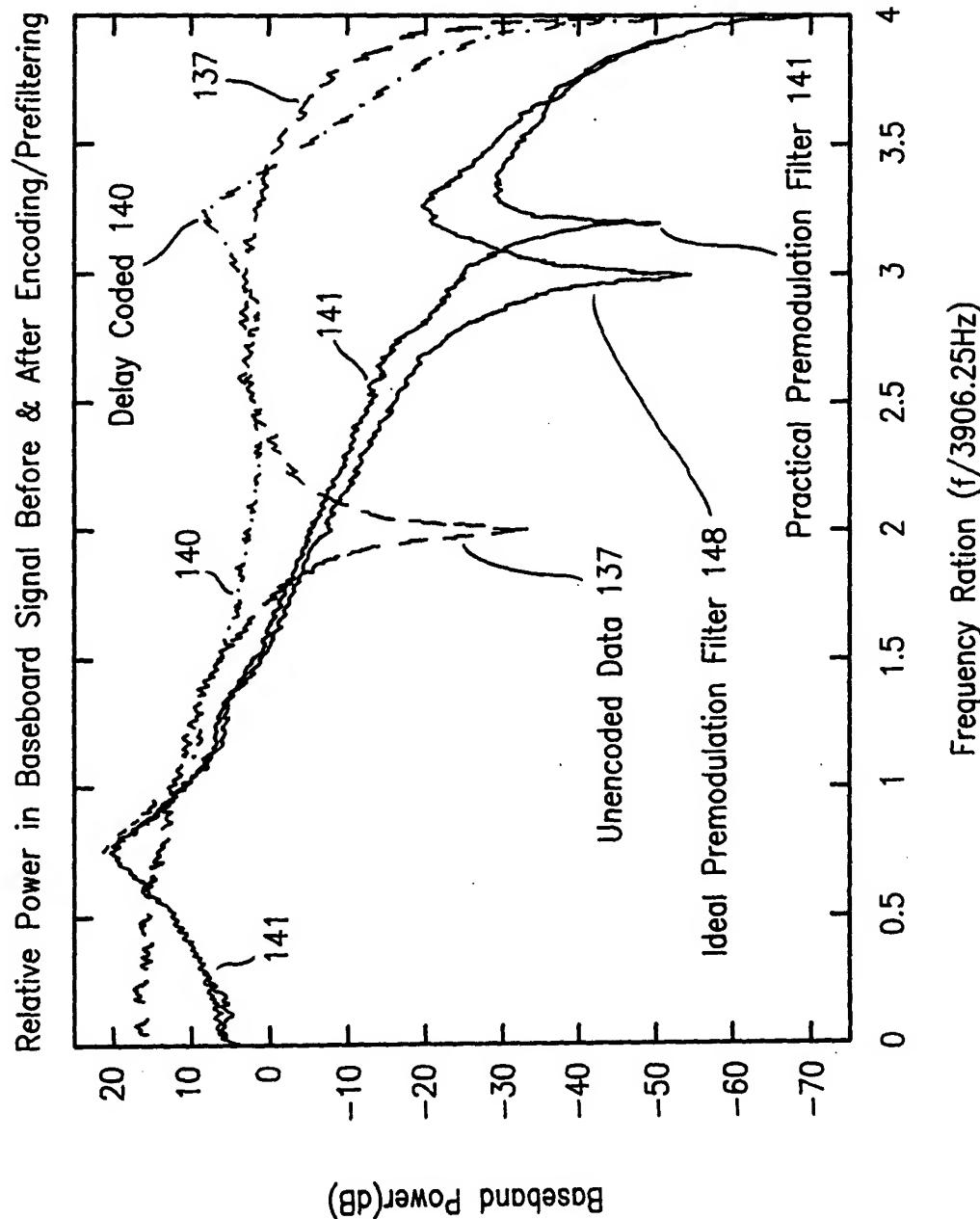


FIG. 19



**FIG. 20**

**FIG. 21**

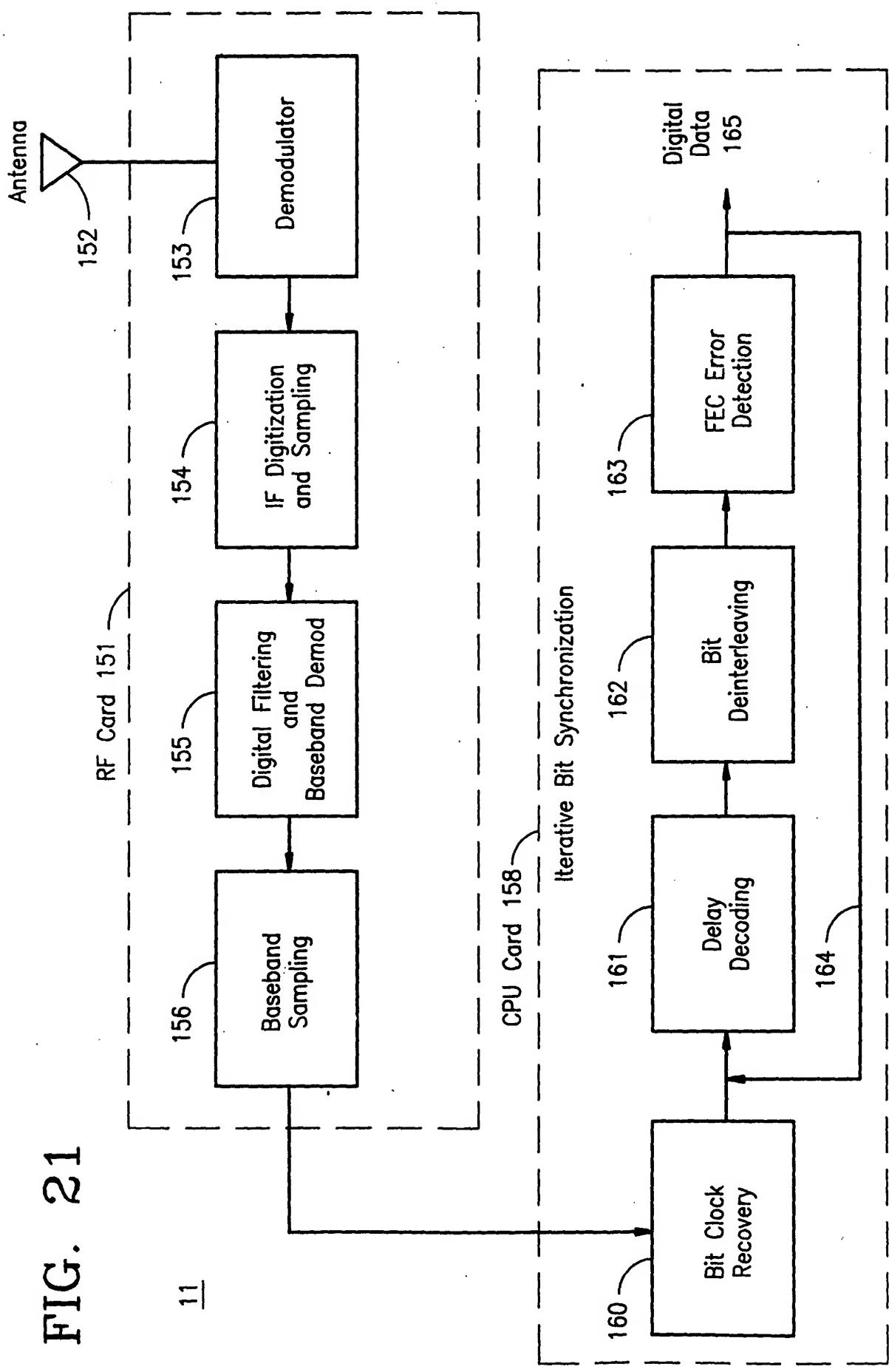
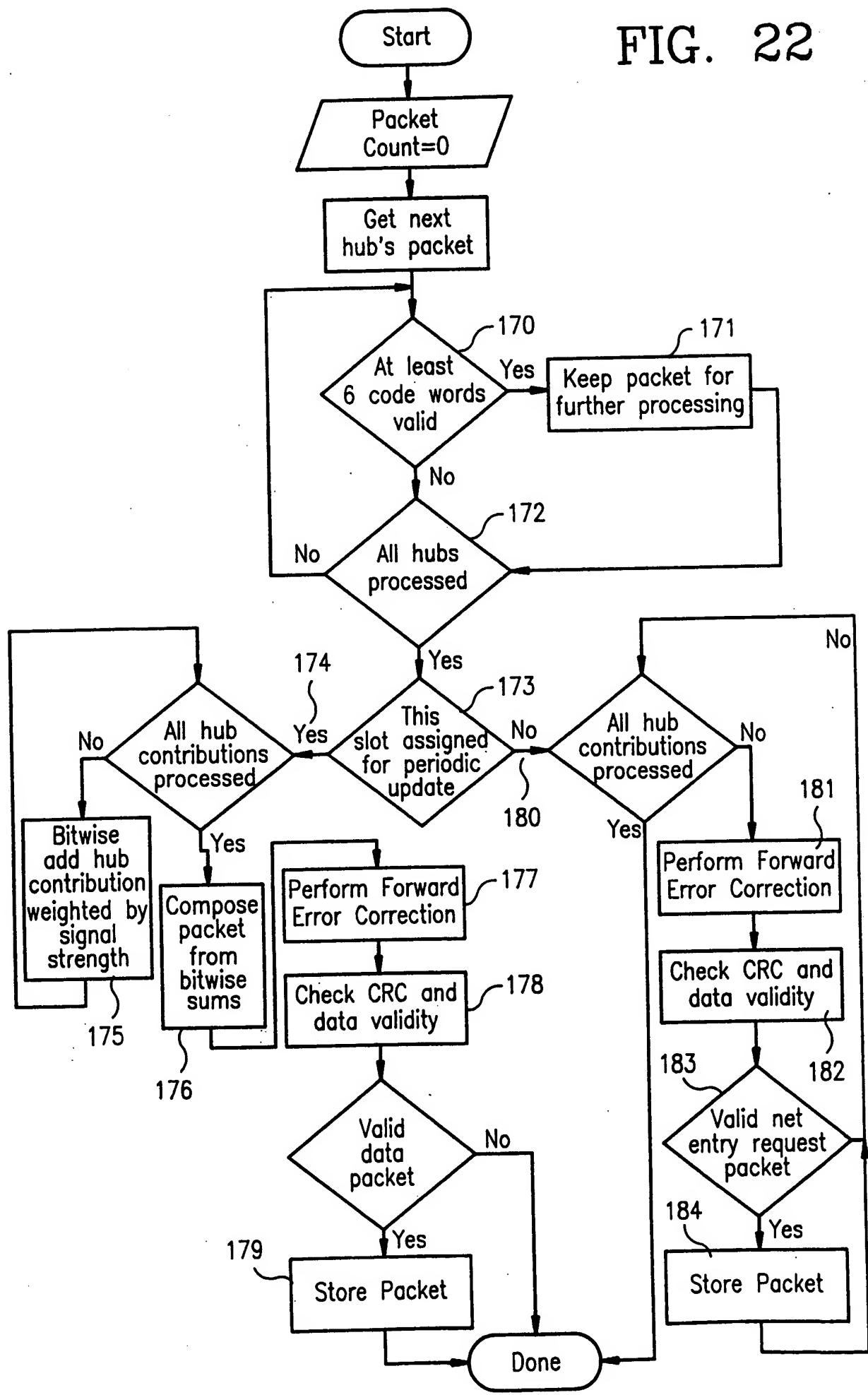
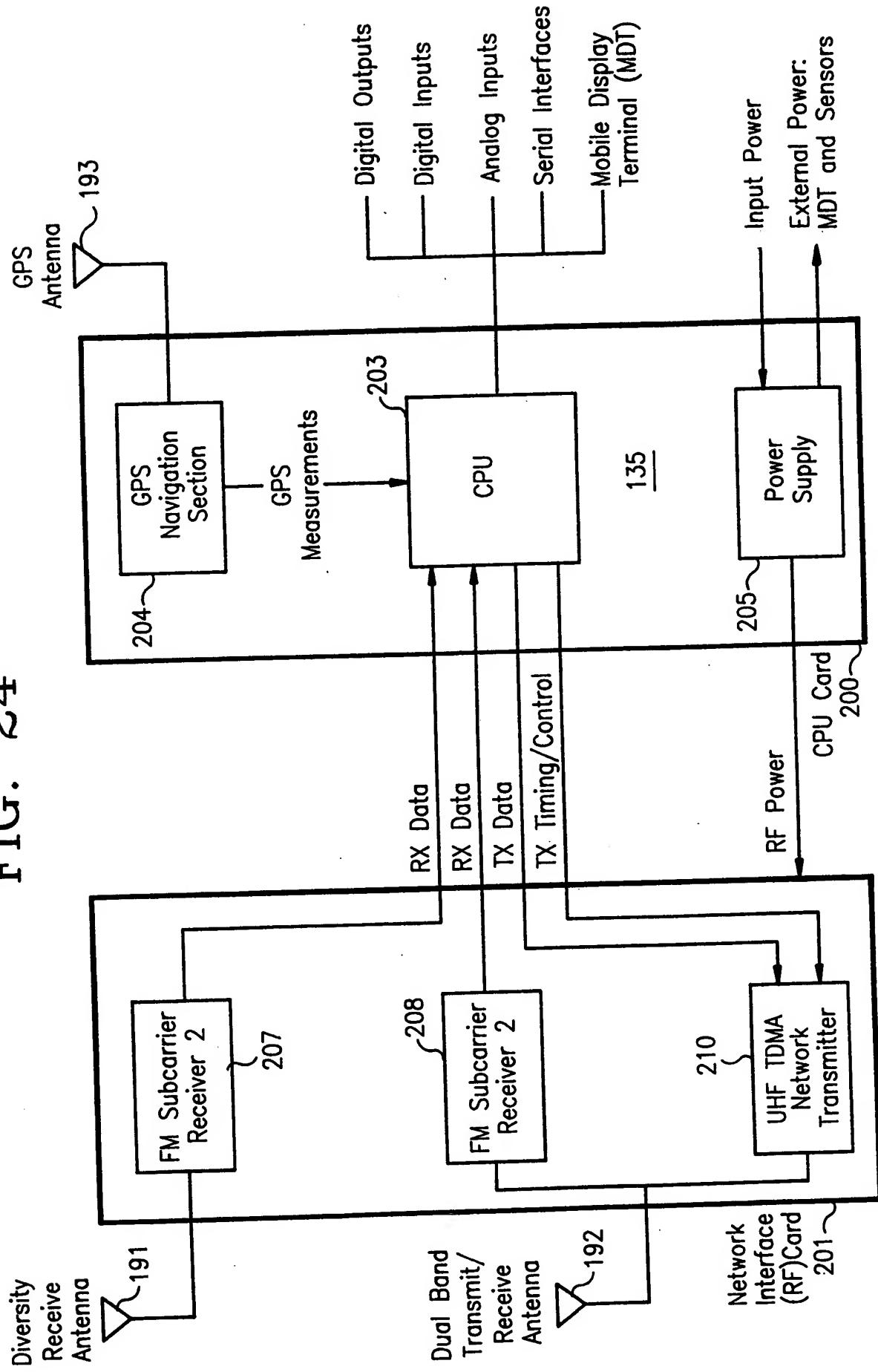
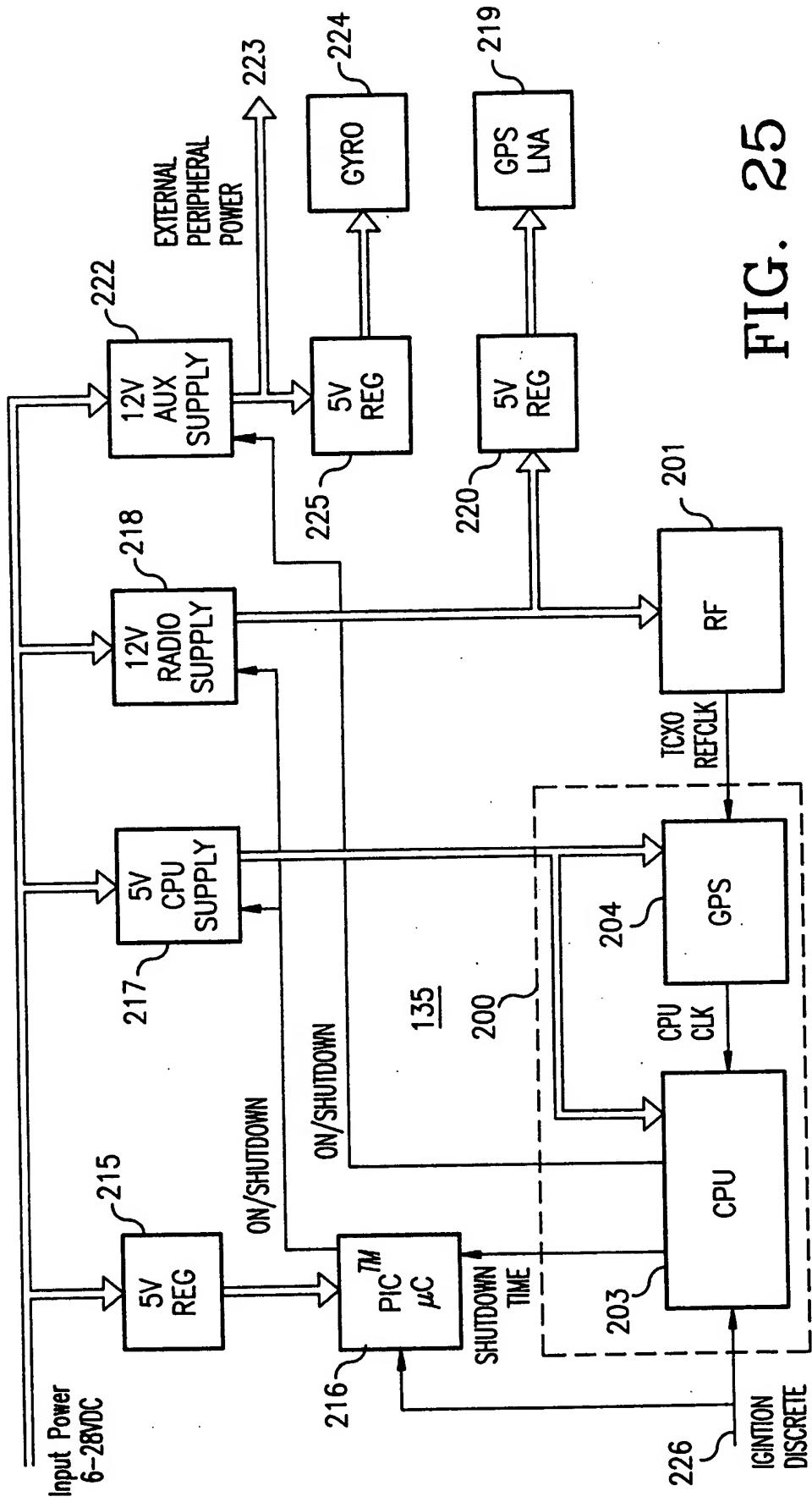


FIG. 22



**FIG. 24**





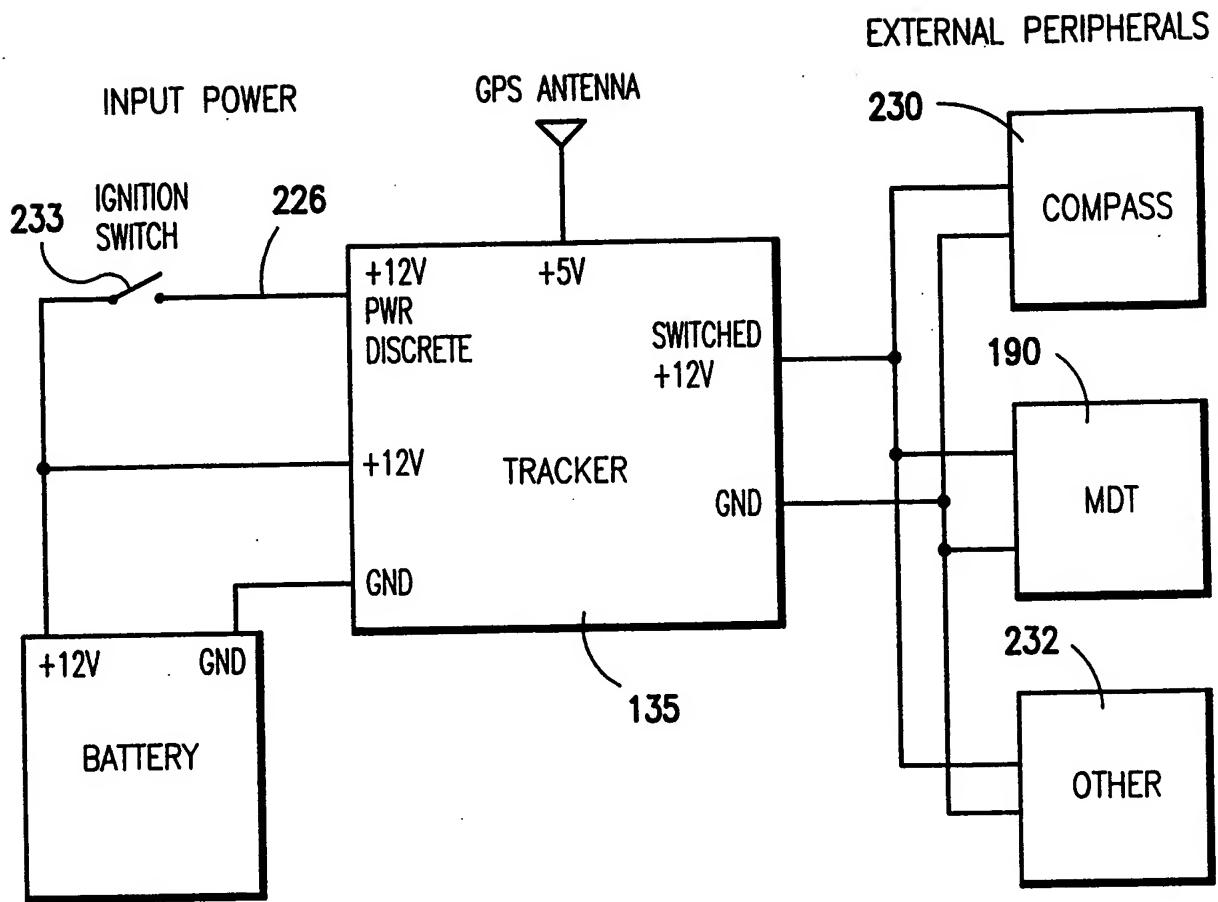


FIG. 26

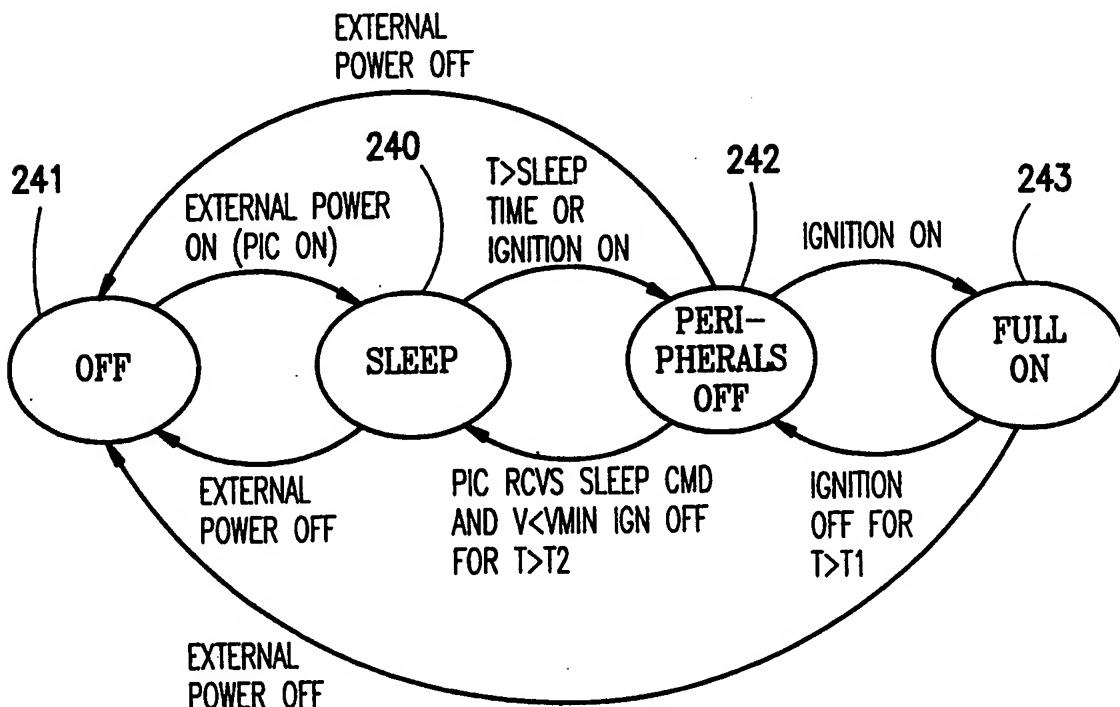


FIG. 27

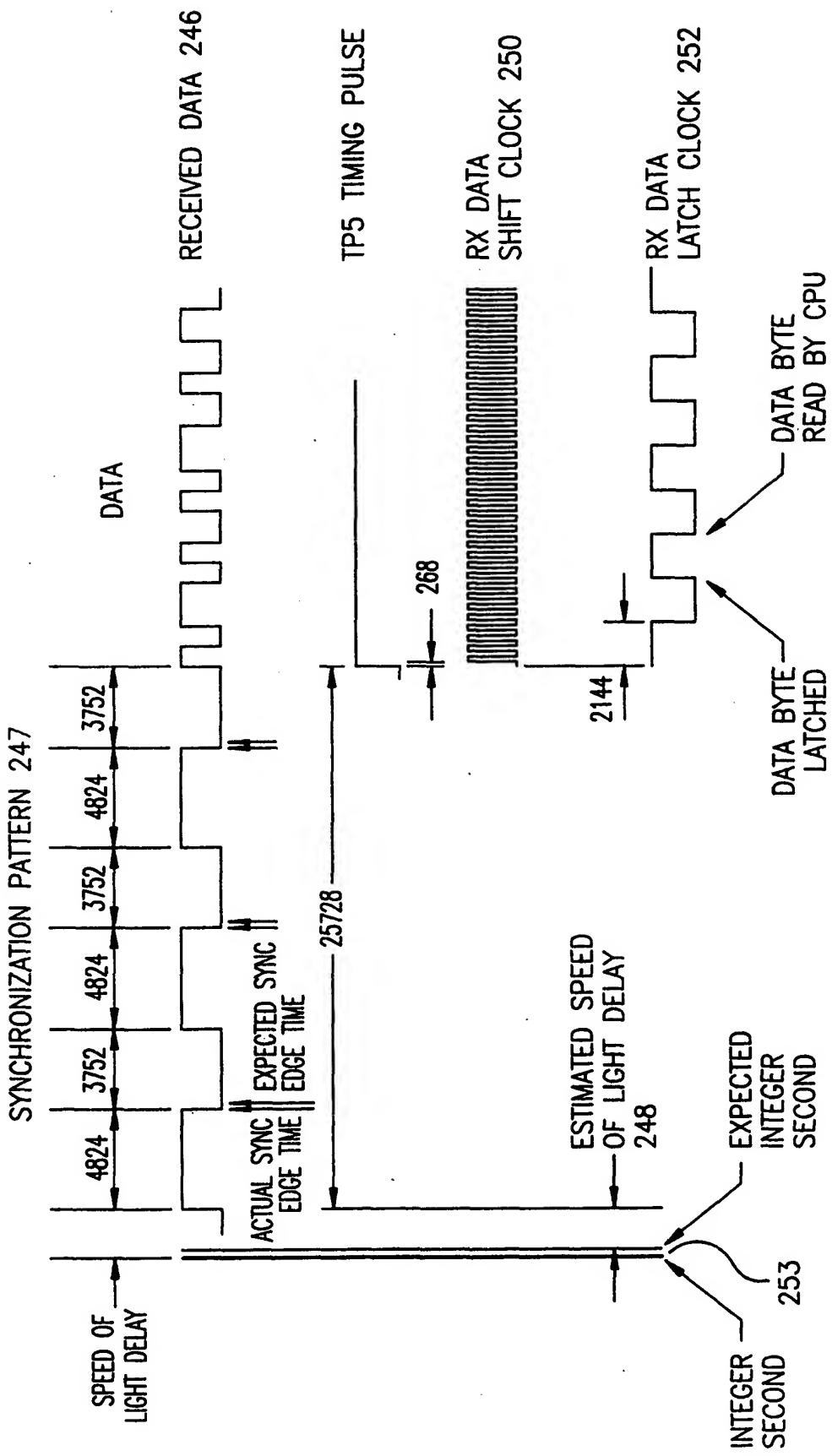
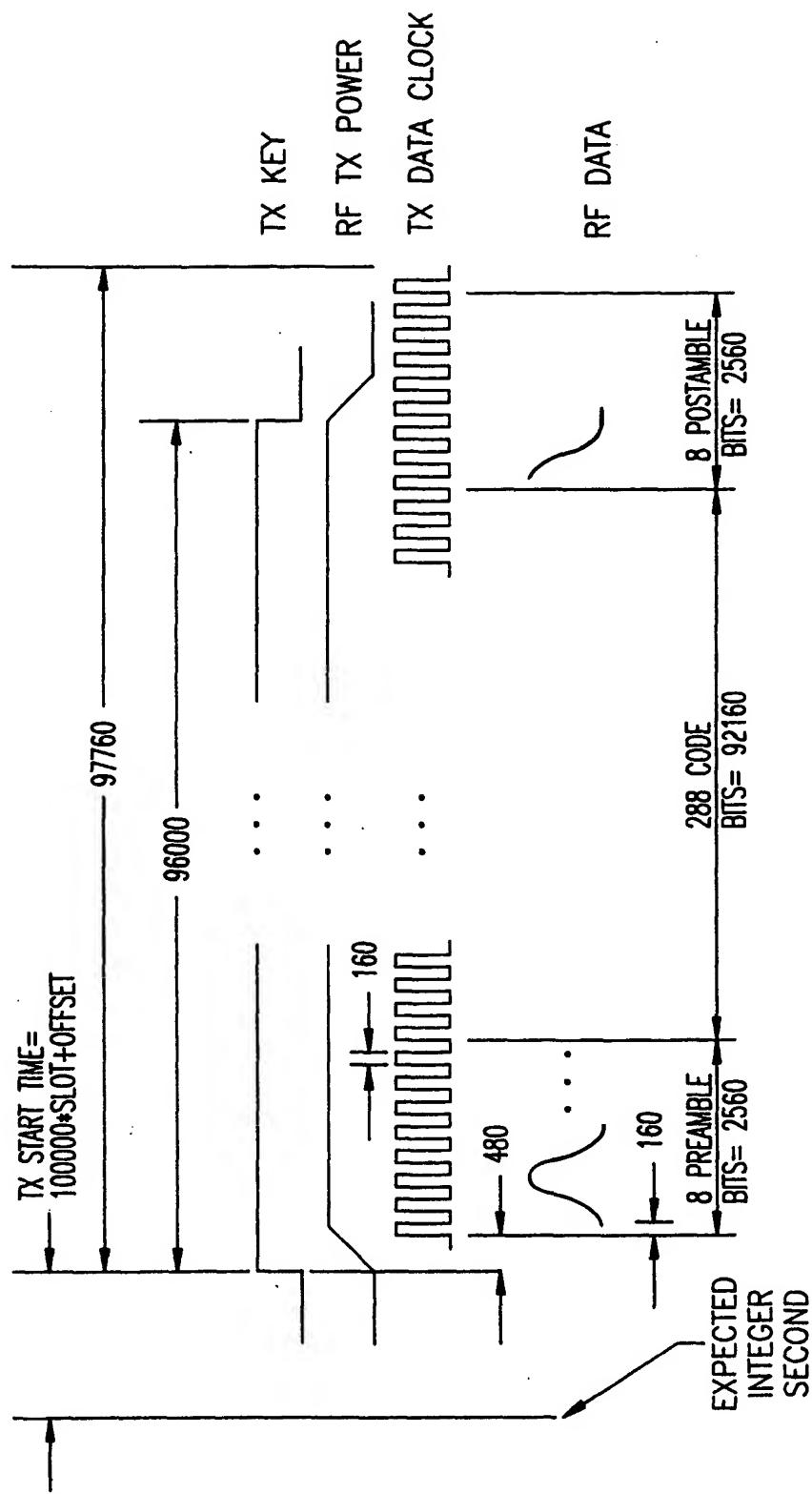


FIG. 28

FIG. 29



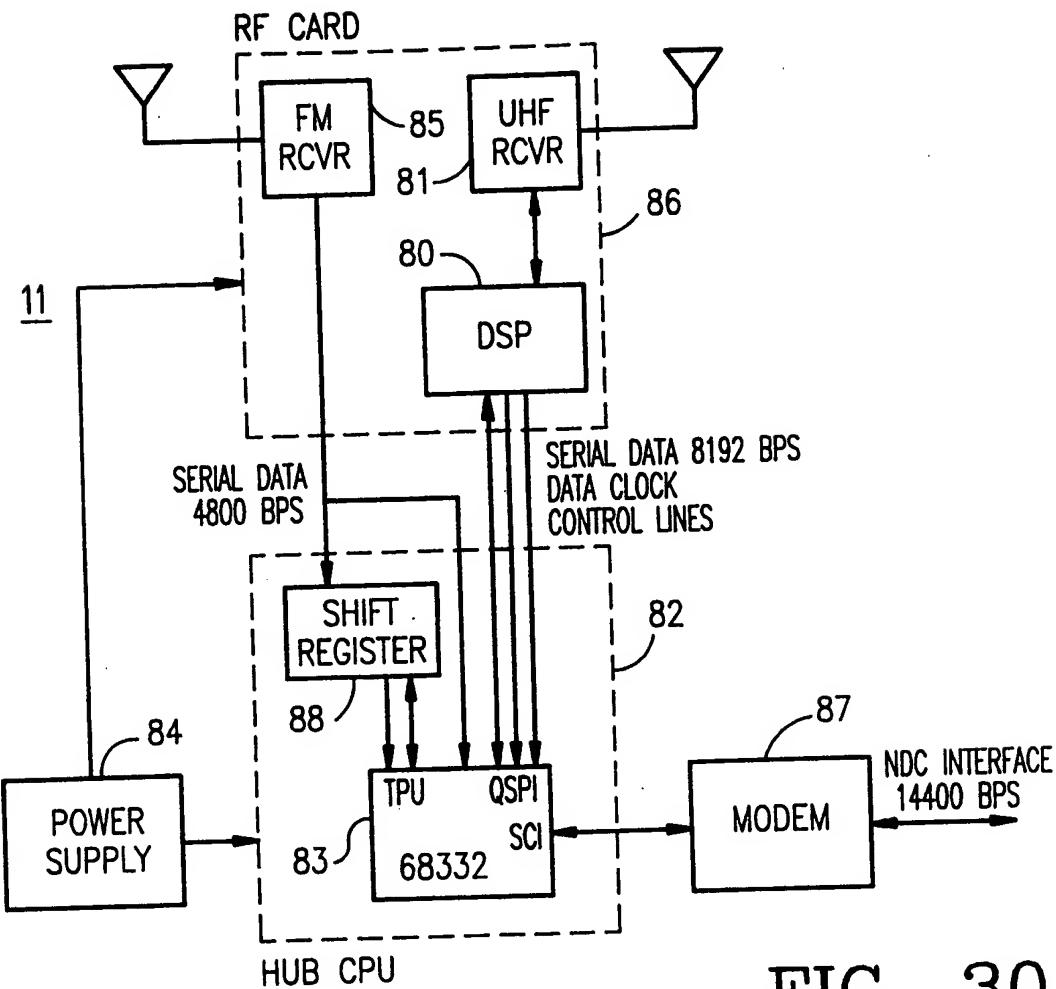


FIG. 30

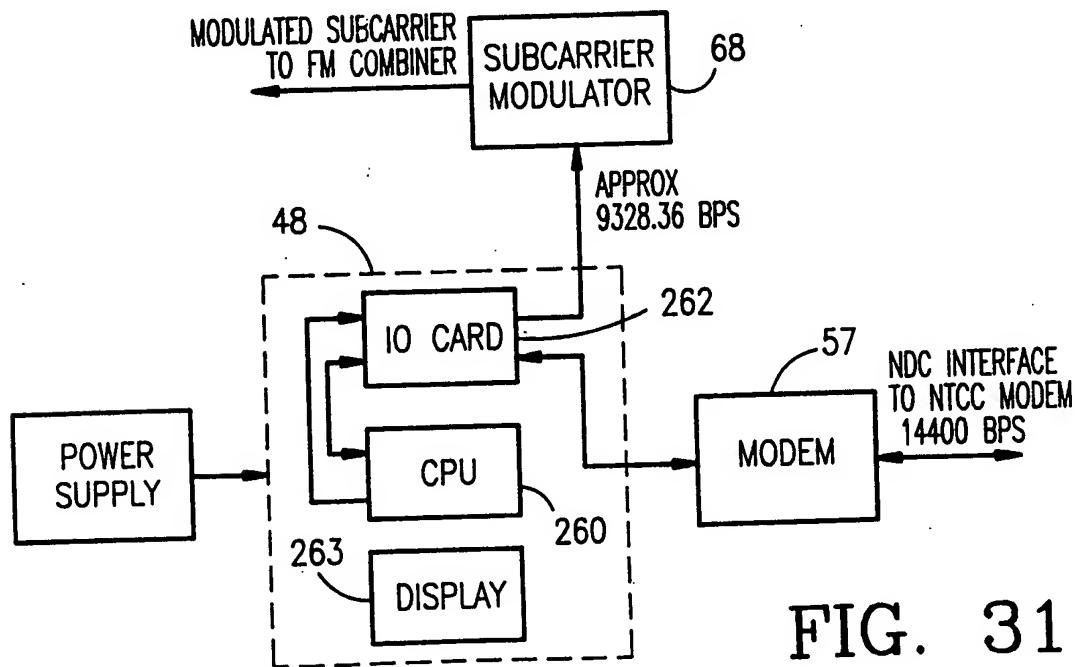


FIG. 31

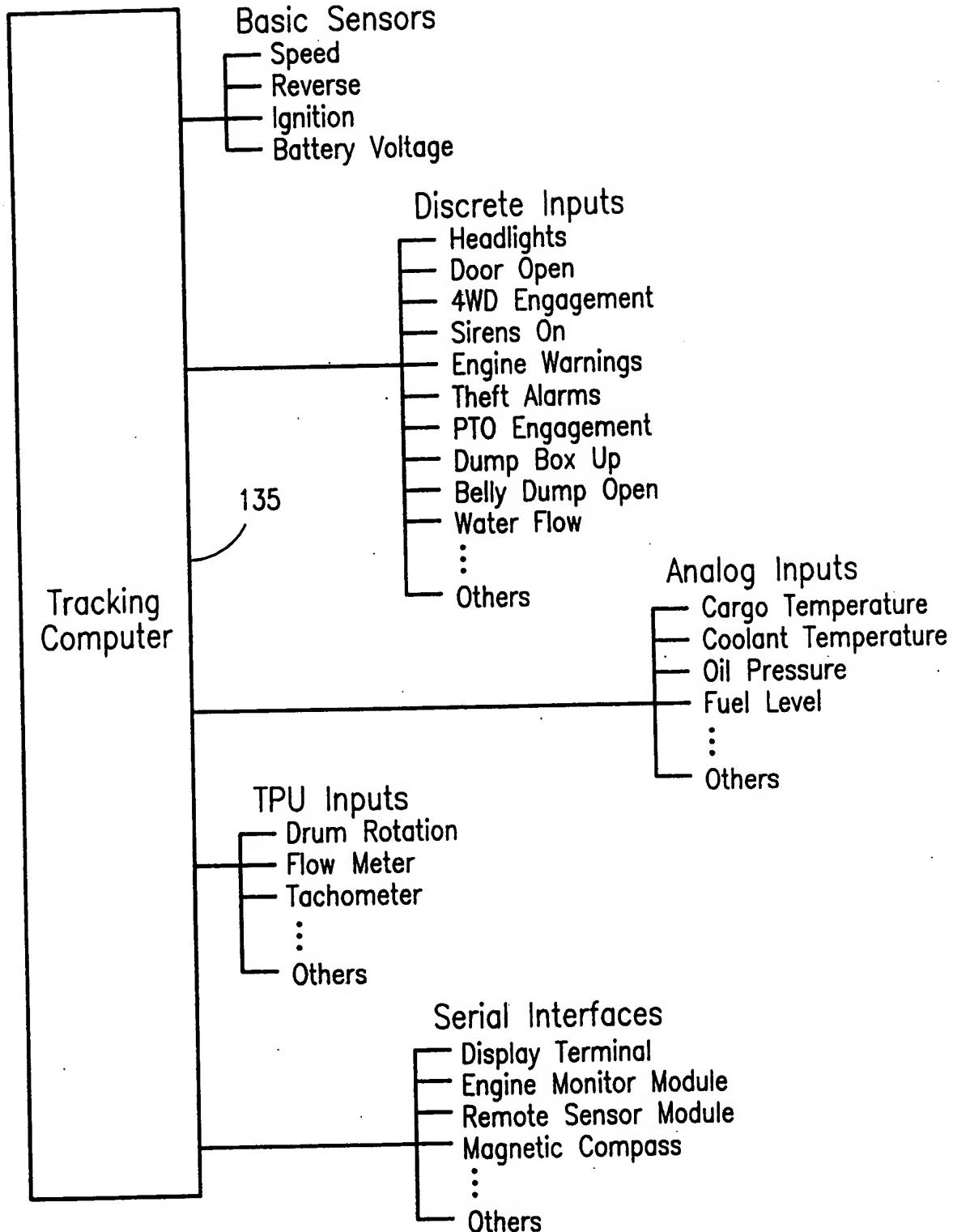


FIG. 33

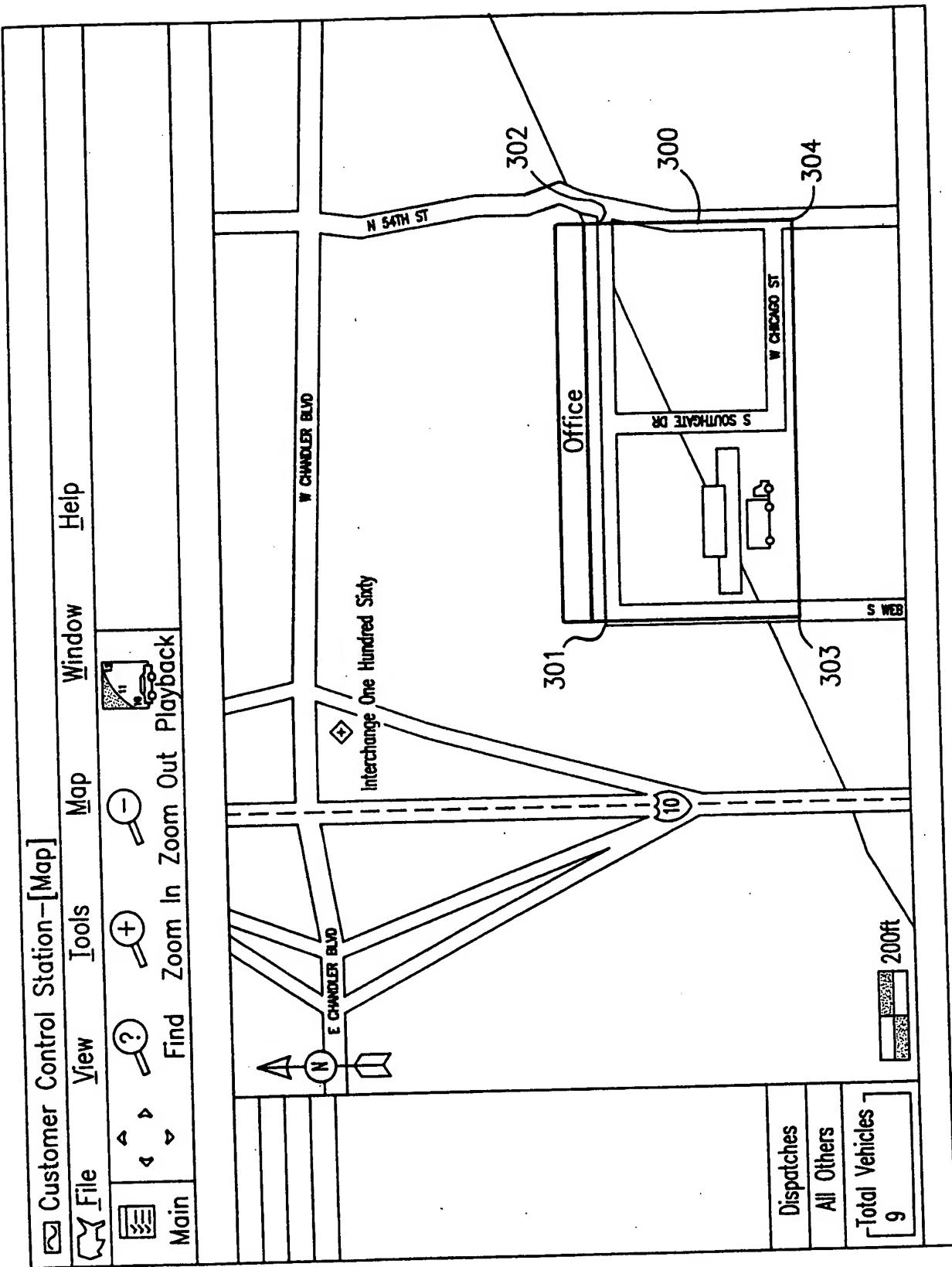


FIG. 34

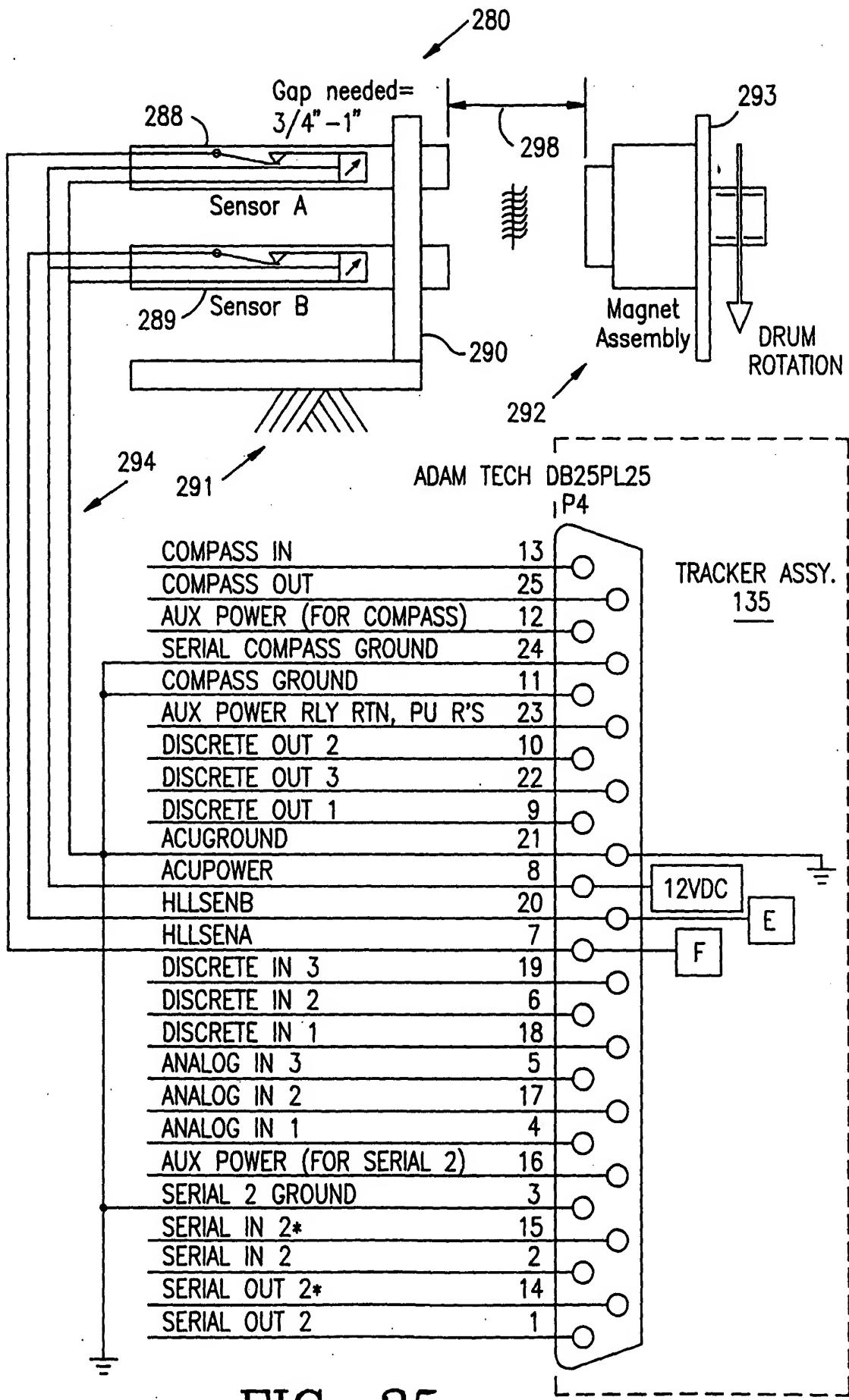


FIG. 35

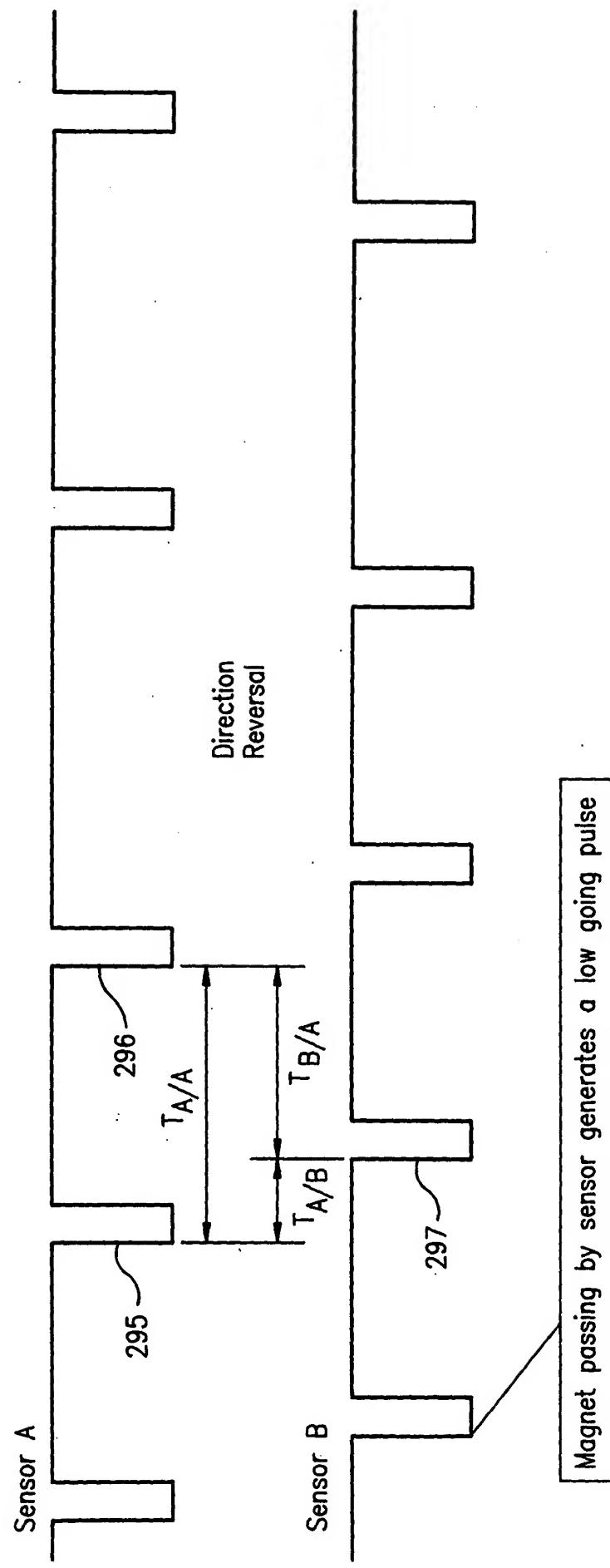


FIG. 36

FIG. 37

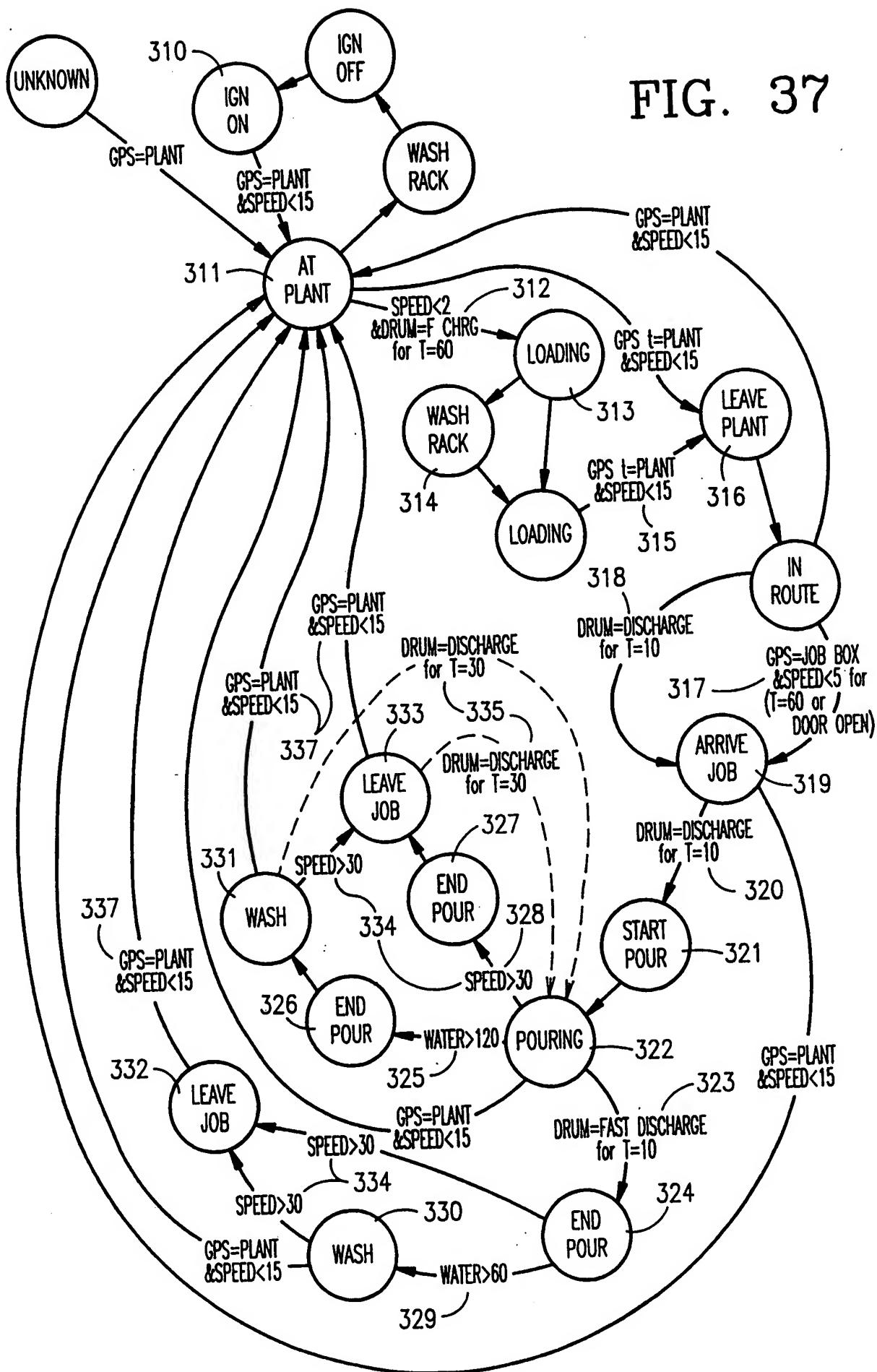


FIG. 38

